

Model Name: GA-H61M-D2H

Revision 1.0

SHEET

TITLE

| | |
|----|----------------------------|
| 01 | COVER SHEET |
| 02 | BOM & PCB MODIFY HISTORY |
| 03 | BLOCK DIAGRAM |
| 04 | CPU_LGA1155-A |
| 05 | CPU_LGA1155-B |
| 06 | CPU_LGA1155-C |
| 07 | DDR III CHANNEL A 1,2 |
| 08 | DDR III CHANNEL B 1,2 |
| 09 | PCH_FDI,DMI,USB,PCIE,NVRAM |
| 10 | PCH_DP,CLK BUFFER |
| 11 | PCH_HOST,SATA,PCI |
| 12 | PCH_GPIO,CTRL,AUDIO |
| 13 | PCH_PWR,GND |
| 14 | PCI EXPRESS*16 SLOT |
| 15 | PCI EXPRESS*1 SLOT/CLK GEN |
| 16 | ITE 8728 |
| 17 | KB_MS,R_USB,-PROCHOT,RI |
| 18 | HWM,FAN CTRL,OV,COMB,LPT |
| 19 | DUAL BIOS |
| 20 | FP,F_USB,SPKR,SATA LED |
| 21 | AUDIO ALC889 |
| 22 | REAR AUDIO JACK |
| 23 | ATHEROS AR8151/USB_LAN |
| 24 | HDMI/DVI |
| 25 | DISCRETE POWER |
| 26 | ATX |
| 27 | ISL95870_CPU_VTT |

SHEET

TITLE

| | |
|----|-----------------------|
| 28 | VCORE ISL6364_1 |
| 29 | VCORE ISL6364_2,VAXG |
| 30 | VCORE ISL6364_3,VCORE |
| 31 | Etron USB3.0 |
| 32 | IT8892E |
| 33 | PCI SLOT 1.2 |
| | |

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Gigabyte Technology

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|-------------|---------------------------|---------------|
| Cover Sheet | | |
| Size | Document Number | Rev |
| Custom | GA-H61M-D2H | 1.0 |
| Date: | Thursday, August 25, 2011 | Sheet 1 of 33 |

Revision 1.0

Component value change history

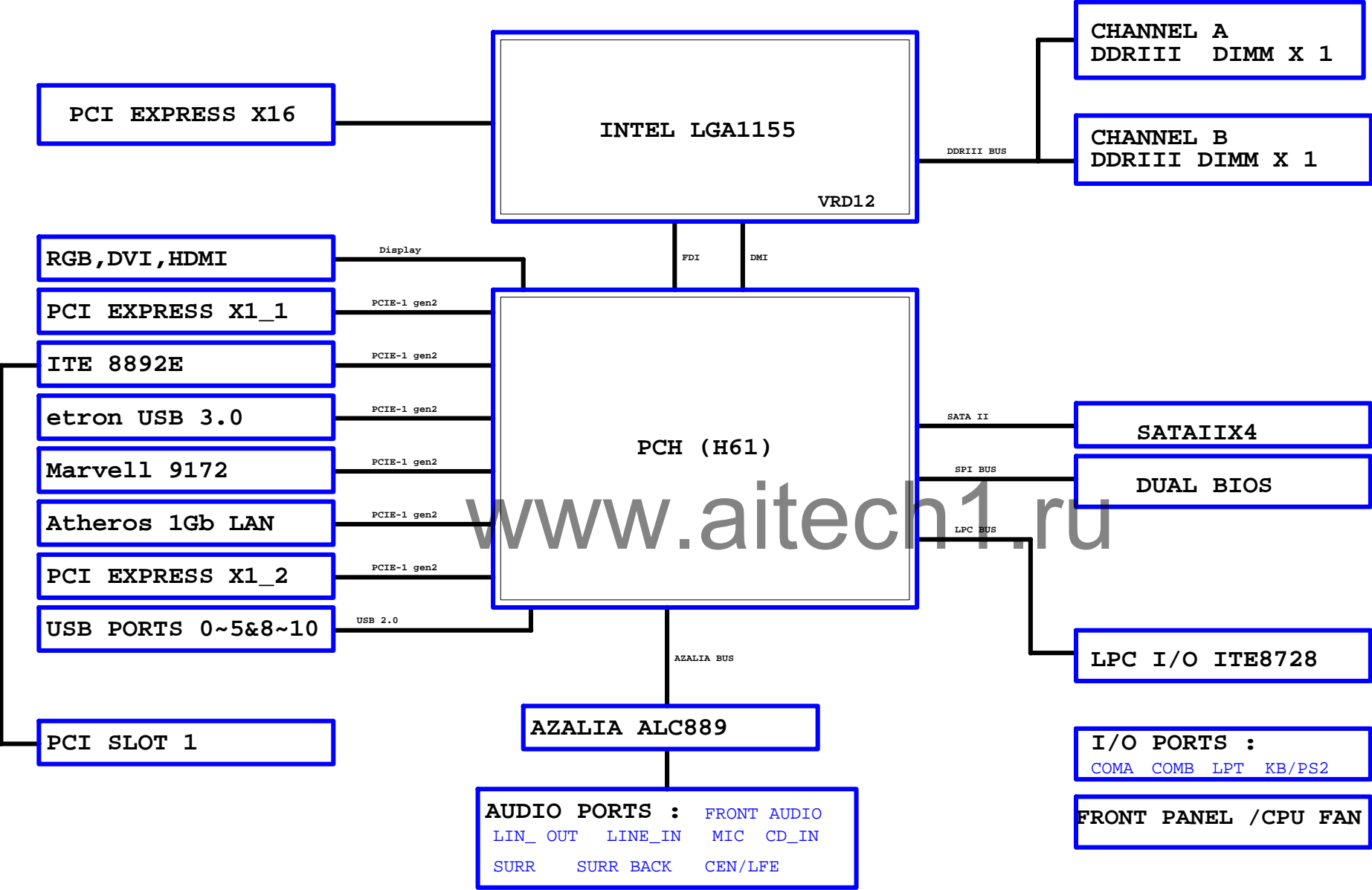
2011/03/25

[illegible]

Circuit or PCB layout change

[illegible]

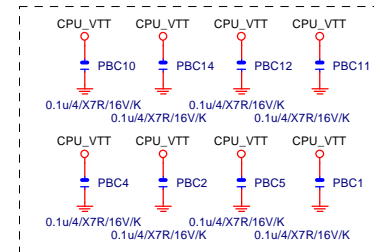
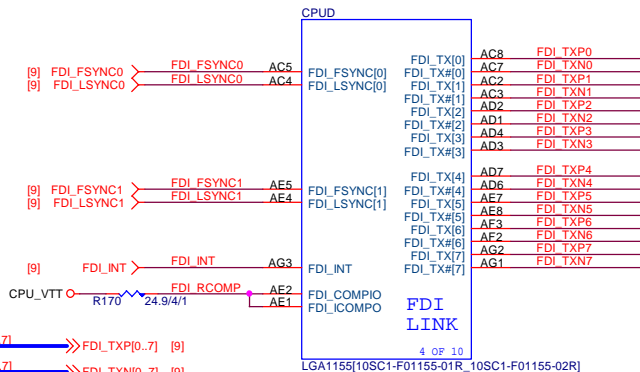
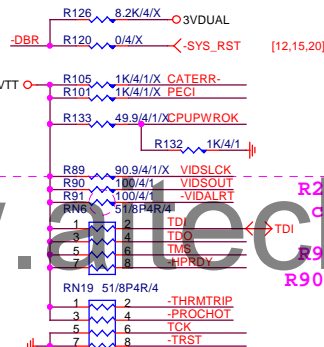
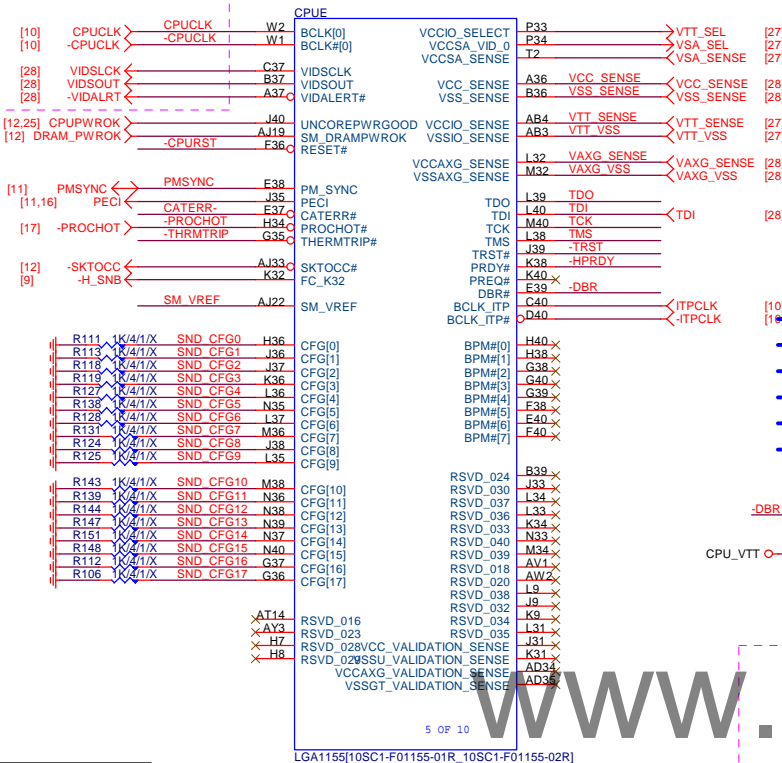
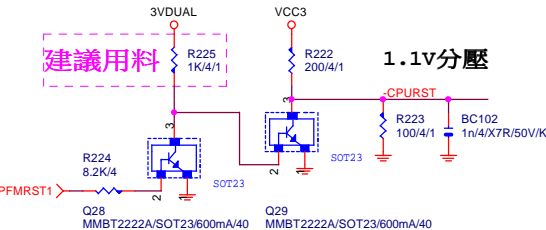
BLOCK DIAGRAM



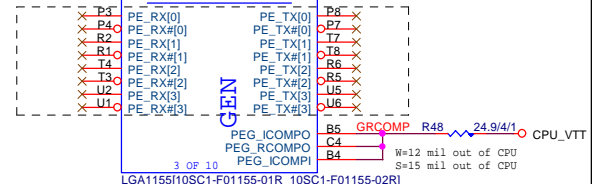
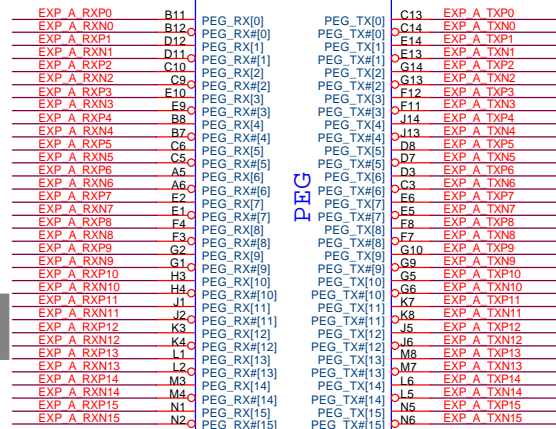
| CFG | H | L | NOTE |
|-----|-------------|----------------|-----------------------|
| 0 | RSVD | RSVD | RSVD |
| 1 | RSVD | RSVD | RSVD |
| 2 | MMIO | Reverse | LANE REVERSAL[0], x16 |
| 3 | RSVD | RSVD | RSVD |
| 4 | RSVD | RSVD | RSVD |
| 7 | RSVD | RSVD | RSVD |
| 8 | RSVD | RSVD | RSVD |
| 9 | RSVD | RSVD | RSVD |
| 10 | RSVD | RSVD | RSVD |
| 11 | RSVD | RSVD | RSVD |
| 12 | RSVD | RSVD | RSVD |
| 13 | RSVD | RSVD | RSVD |
| 14 | RSVD | RSVD | RSVD |
| 15 | RSVD | RSVD | RSVD |
| 16 | RSVD | RSVD | RSVD |
| 17 | RSVD | RSVD | RSVD |

| CFG6 | CFG5 | PCIE CONFIG |
|------|------|----------------|
| 1 | 1 | 1X16 , Default |
| 1 | 0 | 2X8 |
| 0 | 1 | RSVD |
| 0 | 0 | X8,X4,X4 |

CFG 0-17 all internal PULL-UP



Stitching caps for PCIE,DMI,FDI bus



Gigabyte Technology

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|---------------|---------------------------|-------|---------|
| Title | | | |
| CPU LGA1155-A | | | |
| Size | Document Number | Rev | |
| Custom | GA-H61M-D2H | 1.0 | |
| Date: | Thursday, August 25, 2011 | Sheet | 4 of 33 |

CPUA

| | | |
|--------|------|-----------|
| MAAA0 | AV27 | SA_MA[0] |
| MAAA1 | AY24 | SA_MA[1] |
| MAAA2 | AW24 | SA_MA[2] |
| MAAA3 | AW23 | SA_MA[3] |
| MAAA4 | AV23 | SA_MA[4] |
| MAAA5 | AT24 | SA_MA[5] |
| MAAA6 | AT23 | SA_MA[6] |
| MAAA7 | AU22 | SA_MA[7] |
| MAAA8 | AV22 | SA_MA[8] |
| MAAA9 | AT22 | SA_MA[9] |
| MAAA10 | AV28 | SA_MA[10] |
| MAAA11 | AU21 | SA_MA[11] |
| MAAA12 | AT21 | SA_MA[12] |
| MAAA13 | AW32 | SA_MA[13] |
| MAAA14 | AU20 | SA_MA[14] |
| MAAA15 | AT20 | SA_MA[15] |

| | | |
|-----------|-----|--------|
| SA_DQS[0] | AK3 | DQSA0 |
| SA_DQS[0] | AK2 | -DQSA0 |
| SA_DQ[0] | AJ3 | MDA0 |
| SA_DQ[1] | AJ4 | MDA1 |
| SA_DQ[2] | AL3 | MDA2 |
| SA_DQ[3] | AL4 | MDA3 |
| SA_DQ[4] | AJ2 | MDA4 |
| SA_DQ[5] | AJ1 | MDA5 |
| SA_DQ[6] | AL2 | MDA6 |
| SA_DQ[7] | AL1 | MDA7 |

| | | |
|-----------|-----|--------|
| SA_DQS[1] | AP3 | DQSA1 |
| SA_DQS[1] | AP2 | -DQSA1 |
| SA_DQ[8] | AN1 | MDA8 |
| SA_DQ[9] | AN4 | MDA9 |
| SA_DQ[10] | AR3 | MDA10 |
| SA_DQ[11] | AR4 | MDA12 |
| SA_DQ[12] | AN2 | MDA11 |
| SA_DQ[13] | AN3 | MDA13 |
| SA_DQ[14] | AR2 | MDA14 |
| SA_DQ[15] | AR1 | MDA15 |

| | | |
|----------|------|------|
| SA_BS[0] | SBA0 | AY29 |
| SA_BS[1] | SBA1 | AW28 |
| SA_BS[2] | SBA2 | AV20 |
| SA_CS[0] | CSA0 | AY29 |
| SA_CS[1] | CSA1 | AV32 |
| SA_CS[2] | AW30 | AY30 |
| SA_CS[3] | AW33 | AY33 |

| | | |
|-----------|---------|------|
| SA_CKE[0] | CKE0 | AY19 |
| SA_CKE[1] | CKE1 | AT19 |
| SA_CKE[2] | AU18 | AY18 |
| SA_CKE[3] | AV18 | AY18 |
| SA_ODT[0] | MODT_A0 | AV31 |
| SA_ODT[1] | MODT_A1 | AU32 |
| SA_ODT[2] | AU30 | AY30 |
| SA_ODT[3] | AW33 | AY33 |

| | | |
|-----------|-----|--------|
| SA_DQS[2] | AW4 | DQSA2 |
| SA_DQS[2] | AW4 | -DQSA2 |
| SA_DQ[16] | AV2 | MDA16 |
| SA_DQ[17] | AW3 | MDA17 |
| SA_DQ[18] | AV5 | MDA18 |
| SA_DQ[19] | AU2 | MDA20 |
| SA_DQ[20] | AU3 | MDA21 |
| SA_DQ[21] | AU5 | MDA22 |
| SA_DQ[22] | AY5 | MDA23 |
| SA_DQ[23] | AY5 | MDA23 |

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|-----------|-----|--------|
| SA_DQS[3] | AW8 | DQSA3 |
| SA_DQS[3] | AW8 | -DQSA3 |
| SA_CK[0] | AY7 | MDA24 |
| SA_CK[1] | AU7 | MDA25 |
| SA_CK[2] | AY9 | MDA26 |
| SA_CK[3] | AU9 | MDA27 |
| SA_CK[4] | AY7 | MDA28 |
| SA_CK[5] | AW7 | MDA29 |
| SA_CK[6] | AY9 | MDA30 |
| SA_CK[7] | AY9 | MDA31 |

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|-----------|------|--------|
| SA_DQS[4] | AV37 | DQSA4 |
| SA_DQS[4] | AV36 | -DQSA4 |
| SA_DQ[32] | AJ35 | MDA32 |
| SA_DQ[33] | AW37 | MDA33 |
| SA_DQ[34] | AJ39 | MDA34 |
| SA_DQ[35] | AJ36 | MDA35 |
| SA_DQ[36] | AW35 | MDA36 |
| SA_DQ[37] | AY36 | MDA37 |
| SA_DQ[38] | AJ38 | MDA38 |
| SA_DQ[39] | AJ37 | MDA39 |

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|-----------|------|--------|
| SA_DQS[5] | AP38 | DQSA5 |
| SA_DQS[5] | AP39 | -DQSA5 |
| SA_DQ[40] | AR40 | MDA40 |
| SA_DQ[41] | AR37 | MDA41 |
| SA_DQ[42] | AN38 | MDA42 |
| SA_DQ[43] | AN37 | MDA43 |
| SA_DQ[44] | AR39 | MDA44 |
| SA_DQ[45] | AR38 | MDA45 |
| SA_DQ[46] | AN39 | MDA46 |
| SA_DQ[47] | AN40 | MDA47 |

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|-----------|------|--------|
| SA_DQS[6] | AK38 | DQSA6 |
| SA_DQS[6] | AK39 | -DQSA6 |
| SA_DQ[48] | AL40 | MDA48 |
| SA_DQ[49] | AL37 | MDA49 |
| SA_DQ[50] | AJ38 | MDA50 |
| SA_DQ[51] | AJ37 | MDA51 |
| SA_DQ[52] | AL39 | MDA52 |
| SA_DQ[53] | AL38 | MDA53 |
| SA_DQ[54] | AJ39 | MDA54 |
| SA_DQ[55] | AJ40 | MDA55 |

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|-----------|------|--------|
| SA_DQS[7] | AF38 | DQSA7 |
| SA_DQS[7] | AF39 | -DQSA7 |
| SA_DQ[56] | AG40 | MDA56 |
| SA_DQ[57] | AG37 | MDA57 |
| SA_DQ[58] | AE38 | MDA58 |
| SA_DQ[59] | AE37 | MDA59 |
| SA_DQ[60] | AG39 | MDA60 |
| SA_DQ[61] | AG38 | MDA61 |
| SA_DQ[62] | AE39 | MDA62 |
| SA_DQ[63] | AE40 | MDA63 |

DDR_0

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LGA1155[10SC1-F01155-01R_10SC1-F01155-02R]

CPUB

| | | |
|--------|------|-----------|
| MAAB0 | AK24 | SB_MA[0] |
| MAAB1 | AM20 | SB_MA[1] |
| MAAB2 | AM19 | SB_MA[2] |
| MAAB3 | AK18 | SB_MA[3] |
| MAAB4 | AP19 | SB_MA[4] |
| MAAB5 | AP18 | SB_MA[5] |
| MAAB6 | AM18 | SB_MA[6] |
| MAAB7 | AL18 | SB_MA[7] |
| MAAB8 | AL18 | SB_MA[8] |
| MAAB9 | AY17 | SB_MA[9] |
| MAAB10 | AN23 | SB_MA[10] |
| MAAB11 | AU17 | SB_MA[11] |
| MAAB12 | AT18 | SB_MA[12] |
| MAAB13 | AR26 | SB_MA[13] |
| MAAB14 | AY16 | SB_MA[14] |
| MAAB15 | AV16 | SB_MA[15] |

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|-----------|-----|--------|
| SB_DQS[0] | AH7 | DQSB0 |
| SB_DQS[0] | AH6 | -DQSB0 |
| SB_DQ[0] | AG7 | MDB0 |
| SB_DQ[1] | AG8 | MDB1 |
| SB_DQ[2] | AJ9 | MDB2 |
| SB_DQ[3] | AJ8 | MDB3 |
| SB_DQ[4] | AG5 | MDB4 |
| SB_DQ[5] | AG6 | MDB5 |
| SB_DQ[6] | AJ6 | MDB6 |
| SB_DQ[7] | AJ7 | MDB7 |
| SB_DQS[1] | AM8 | DQSB1 |
| SB_DQS[1] | AL8 | -DQSB1 |

| | | |
|----------|------|--------|
| SB_WE# | AM7 | MDB8 |
| SB_CAS# | AM10 | MDB10 |
| SB_RAS# | AL10 | MDB11 |
| SB_BS[0] | AL6 | MDB12 |
| SB_BS[1] | AL9 | MDB14 |
| SB_BS[2] | AM9 | MDB15 |
| SB_CS[0] | AR8 | DQSB2 |
| SB_CS[1] | AP8 | -DQSB2 |
| SB_CS[2] | AP8 | -DQSB2 |
| SB_CS[3] | AP8 | -DQSB2 |

| | | |
|-----------|------|--------|
| SB_DQ[8] | AP7 | MDB16 |
| SB_DQ[9] | AR7 | MDB17 |
| SB_DQ[10] | AP10 | MDB18 |
| SB_DQ[11] | AR10 | MDB19 |
| SB_DQ[12] | AP6 | MDB20 |
| SB_DQ[13] | AR6 | MDB21 |
| SB_DQ[14] | AP9 | MDB22 |
| SB_DQ[15] | AR9 | MDB23 |
| SB_DQS[2] | AN13 | DQSB3 |
| SB_DQS[2] | AN12 | -DQSB3 |

| | | |
|-----------|------|--------|
| SB_CKE[0] | AM12 | MDB24 |
| SB_CKE[1] | AM13 | MDB25 |
| SB_CKE[2] | AR13 | MDB26 |
| SB_CKE[3] | AP13 | MDB27 |
| SB_ODT[0] | AL12 | MDB28 |
| SB_ODT[1] | AL13 | MDB29 |
| SB_ODT[2] | AR12 | MDB30 |
| SB_ODT[3] | AP12 | MDB31 |
| SB_DQS[3] | AN29 | DQSB4 |
| SB_DQS[3] | AN28 | -DQSB4 |

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|-----------|------|--------|
| SB_DQ[16] | AR28 | MDB32 |
| SB_DQ[17] | AP23 | MDB33 |
| SB_DQ[18] | AL28 | MDB34 |
| SB_DQ[19] | AL29 | MDB35 |
| SB_DQ[20] | AP28 | MDB36 |
| SB_DQ[21] | AP29 | MDB37 |
| SB_DQ[22] | AN28 | MDB38 |
| SB_DQ[23] | AM29 | MDB39 |
| SB_DQS[4] | AP33 | DQSB5 |
| SB_DQS[4] | AR33 | -DQSB5 |

| | | |
|-----------|------|--------|
| SB_DQ[24] | AP32 | MDB40 |
| SB_DQ[25] | AP21 | MDB41 |
| SB_DQ[26] | AP35 | MDB42 |
| SB_DQ[27] | AP34 | MDB43 |
| SB_DQ[28] | AR32 | MDB44 |
| SB_DQ[29] | AR31 | MDB45 |
| SB_DQ[30] | AR35 | MDB46 |
| SB_DQ[31] | AR34 | MDB47 |
| SB_DQS[5] | AL33 | DQSB6 |
| SB_DQS[5] | AM33 | -DQSB6 |

| | | |
|-----------|------|--------|
| SB_DQ[32] | AM32 | MDB48 |
| SB_DQ[33] | AM31 | MDB49 |
| SB_DQ[34] | AL35 | MDB50 |
| SB_DQ[35] | AL32 | MDB51 |
| SB_DQ[36] | AM34 | MDB52 |
| SB_DQ[37] | AL31 | MDB53 |
| SB_DQ[38] | AM35 | MDB54 |
| SB_DQ[39] | AL34 | MDB55 |
| SB_DQS[6] | AG35 | DQSB7 |
| SB_DQS[6] | AG34 | -DQSB7 |

| | | |
|-----------|------|-------|
| SB_DQ[40] | AH35 | MDB56 |
| SB_DQ[41] | AH34 | MDB57 |
| SB_DQ[42] | AE34 | MDB58 |
| SB_DQ[43] | AE35 | MDB59 |
| SB_DQ[44] | AJ35 | MDB60 |
| SB_DQ[45] | AJ34 | MDB61 |
| SB_DQ[46] | AF33 | MDB62 |
| SB_DQ[47] | AF35 | MDB63 |

| | | |
|-----------|------|--------|
| SB_DQS[8] | AN16 | DQSB8 |
| SB_DQS[8] | AN15 | -DQSB8 |
| SB_DQS[9] | AN16 | DQSB8 |
| SB_DQS[9] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[10] | AN16 | DQSB8 |
| SB_DQS[10] | AN15 | -DQSB8 |
| SB_DQS[11] | AN16 | DQSB8 |
| SB_DQS[11] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[12] | AN16 | DQSB8 |
| SB_DQS[12] | AN15 | -DQSB8 |
| SB_DQS[13] | AN16 | DQSB8 |
| SB_DQS[13] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[14] | AN16 | DQSB8 |
| SB_DQS[14] | AN15 | -DQSB8 |
| SB_DQS[15] | AN16 | DQSB8 |
| SB_DQS[15] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[16] | AN16 | DQSB8 |
| SB_DQS[16] | AN15 | -DQSB8 |
| SB_DQS[17] | AN16 | DQSB8 |
| SB_DQS[17] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[18] | AN16 | DQSB8 |
| SB_DQS[18] | AN15 | -DQSB8 |
| SB_DQS[19] | AN16 | DQSB8 |
| SB_DQS[19] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[20] | AN16 | DQSB8 |
| SB_DQS[20] | AN15 | -DQSB8 |
| SB_DQS[21] | AN16 | DQSB8 |
| SB_DQS[21] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[22] | AN16 | DQSB8 |
| SB_DQS[22] | AN15 | -DQSB8 |
| SB_DQS[23] | AN16 | DQSB8 |
| SB_DQS[23] | AN15 | -DQSB8 |

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|------------|------|--------|
| SB_DQS[24] | AN16 | DQSB8 |
| SB_DQS[24] | AN15 | -DQSB8 |
| SB_DQS[25] | AN16 | DQSB8 |
| SB_DQS[25] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[26] | AN16 | DQSB8 |
| SB_DQS[26] | AN15 | -DQSB8 |
| SB_DQS[27] | AN16 | DQSB8 |
| SB_DQS[27] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[28] | AN16 | DQSB8 |
| SB_DQS[28] | AN15 | -DQSB8 |
| SB_DQS[29] | AN16 | DQSB8 |
| SB_DQS[29] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[30] | AN16 | DQSB8 |
| SB_DQS[30] | AN15 | -DQSB8 |
| SB_DQS[31] | AN16 | DQSB8 |
| SB_DQS[31] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[32] | AN16 | DQSB8 |
| SB_DQS[32] | AN15 | -DQSB8 |
| SB_DQS[33] | AN16 | DQSB8 |
| SB_DQS[33] | AN15 | -DQSB8 |

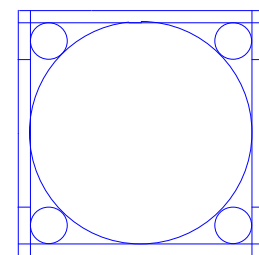
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| SB_DQS[34] | AN15 | -DQSB8 |
| SB_DQS[35] | AN16 | DQSB8 |
| SB_DQS[35] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[36] | AN16 | DQSB8 |
| SB_DQS[36] | AN15 | -DQSB8 |
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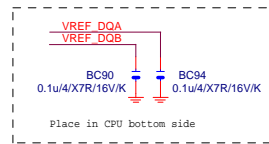
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| SB_DQS[38] | AN16 | DQSB8 |
| SB_DQS[38] | AN15 | -DQSB8 |
| SB_DQS[39] | AN16 | DQSB8 |
| SB_DQS[39] | AN15 | -DQSB8 |

| | | |
|------------|------|--------|
| SB_DQS[40] | AN16 | DQSB8 |
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| SB_DQS[41] | AN16 | DQSB8 |
| SB_DQS[41] | AN15 | -DQSB8 |

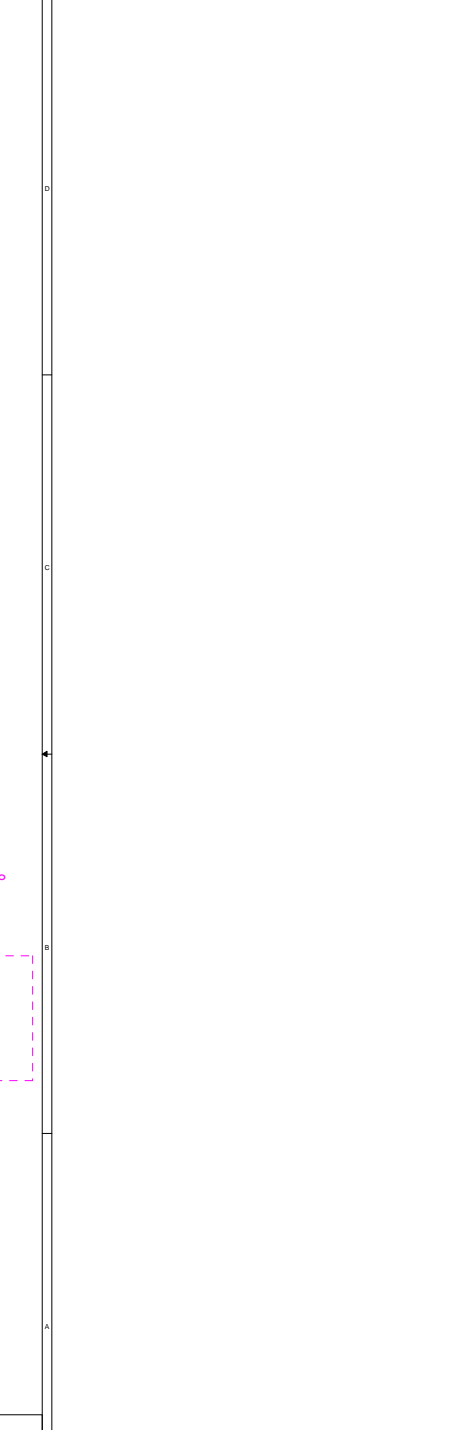
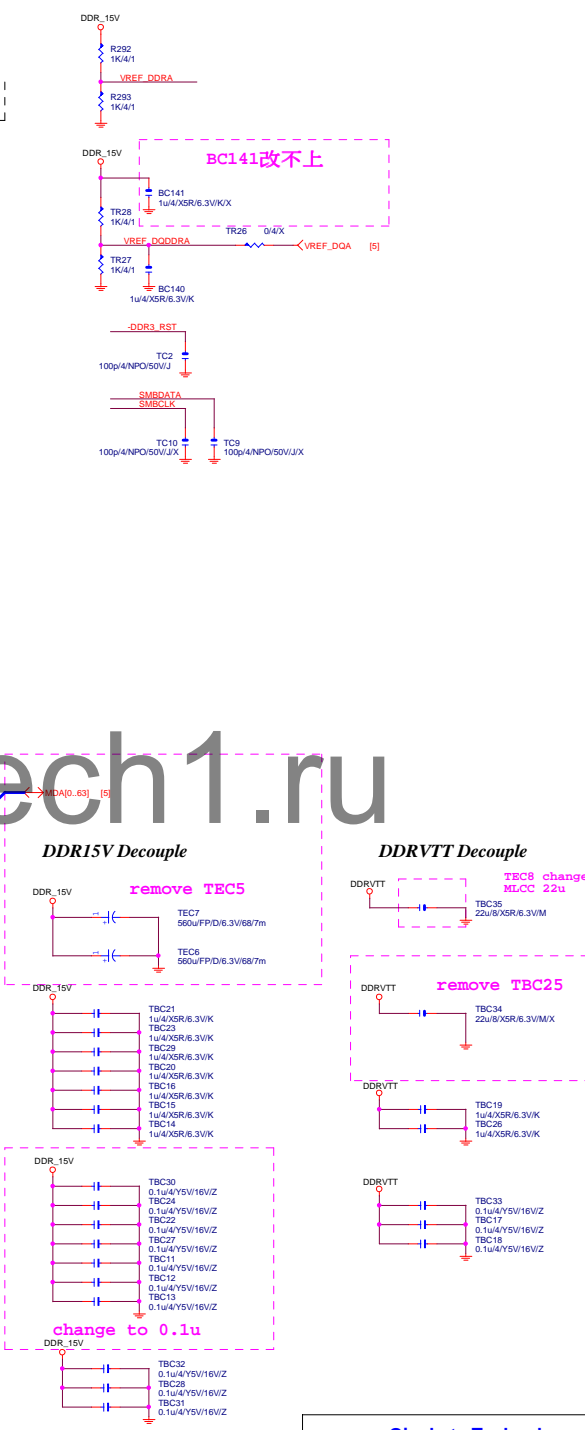
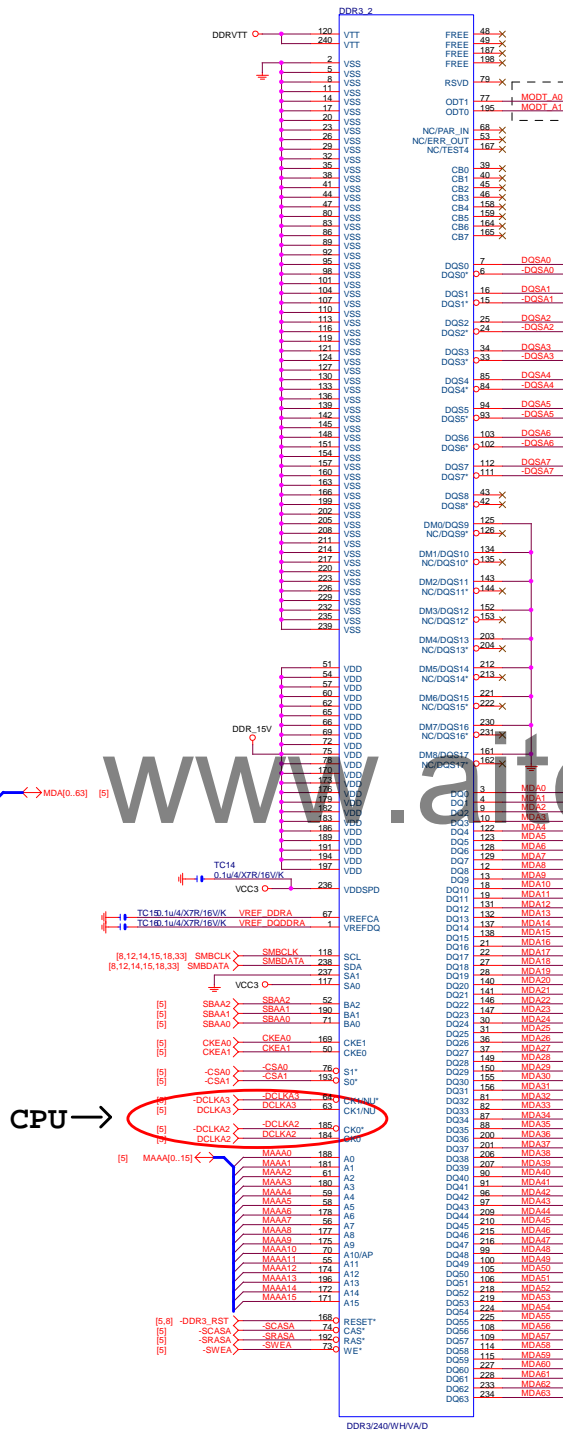
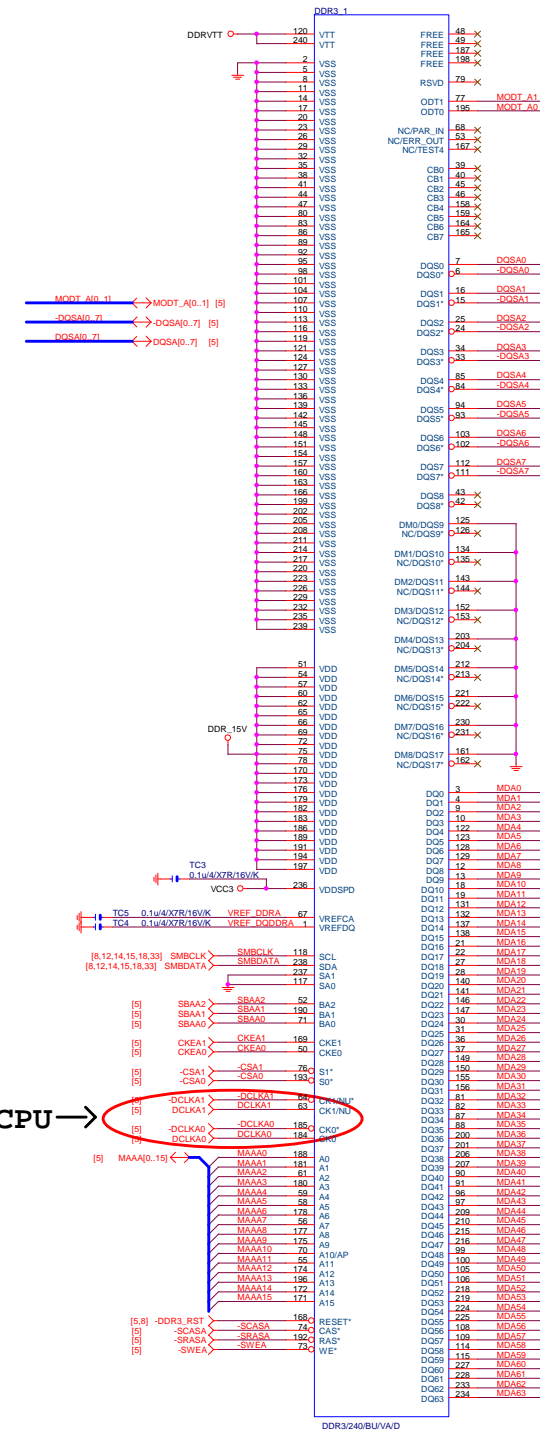
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|------------|------|--------|
| SB_DQS[42] | AN16 | DQSB8 |
| SB_DQS[42] | AN15 | -DQSB8 |
| SB_DQS[43] | AN16 | DQSB8 |
| SB_DQS[43] | AN15 | -DQSB8 |

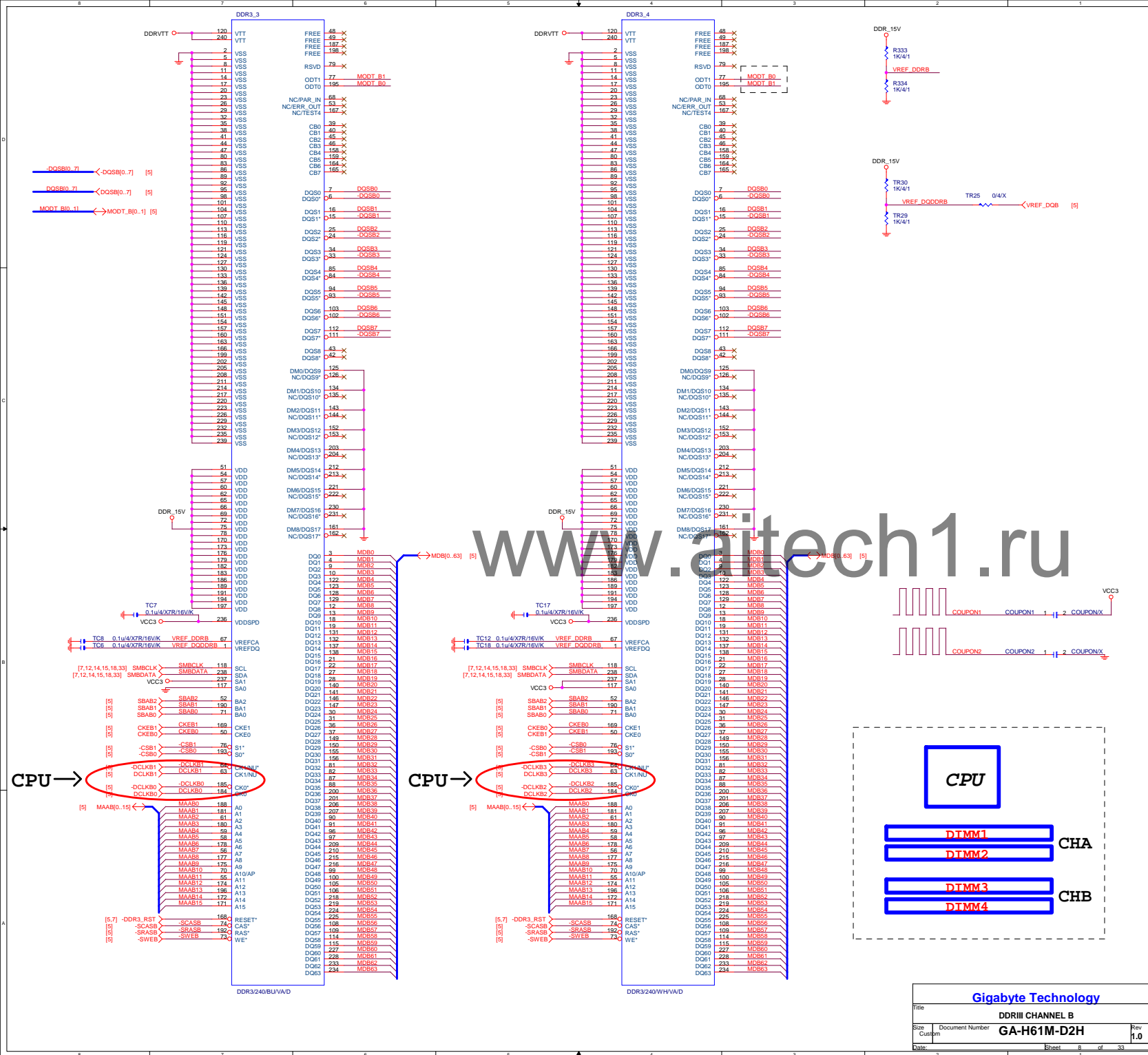
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CPU RETENTION/X

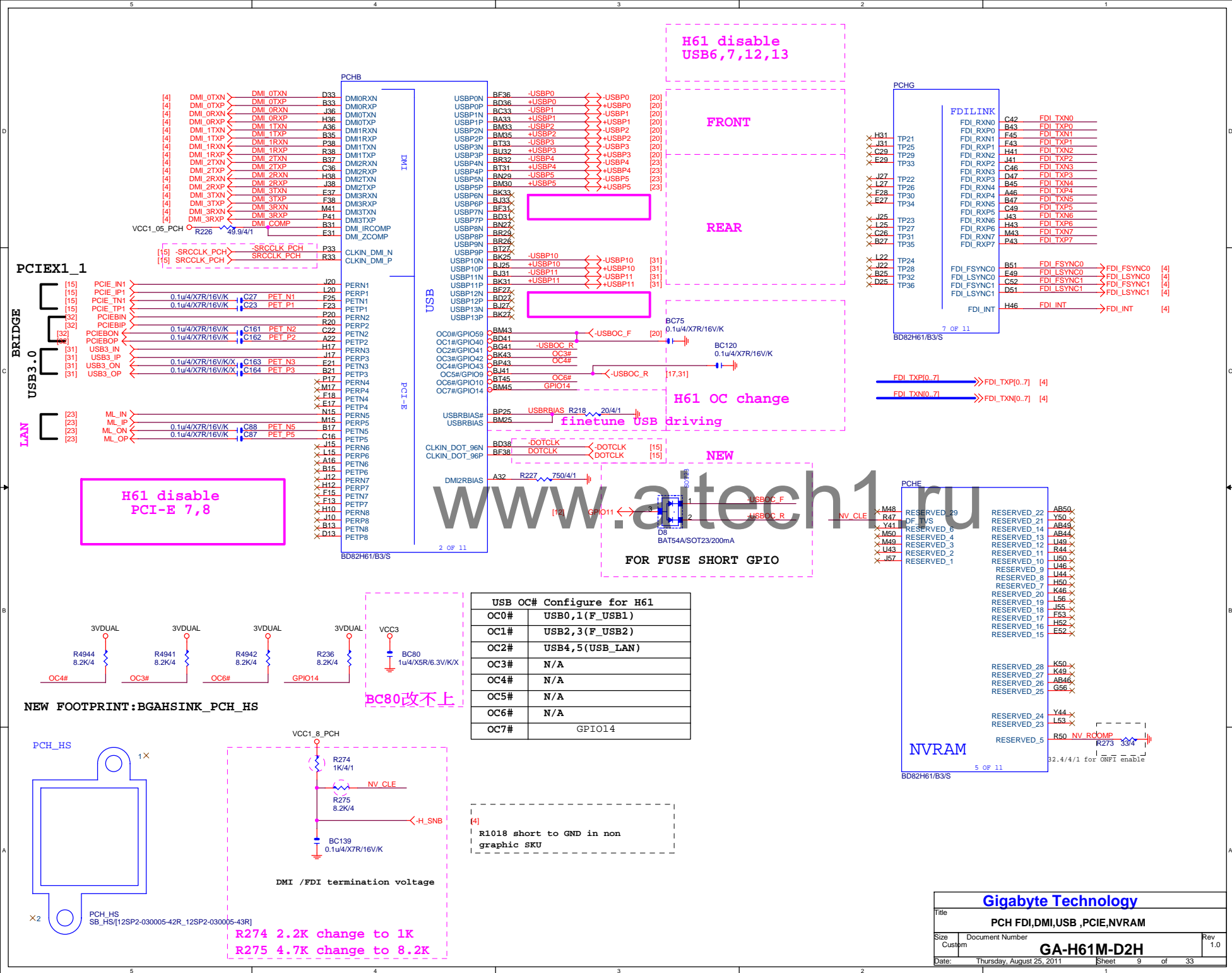
Need check the new CPU ME

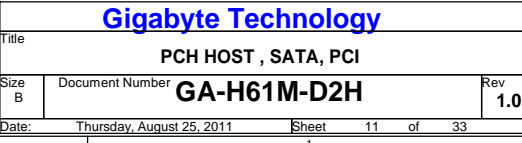


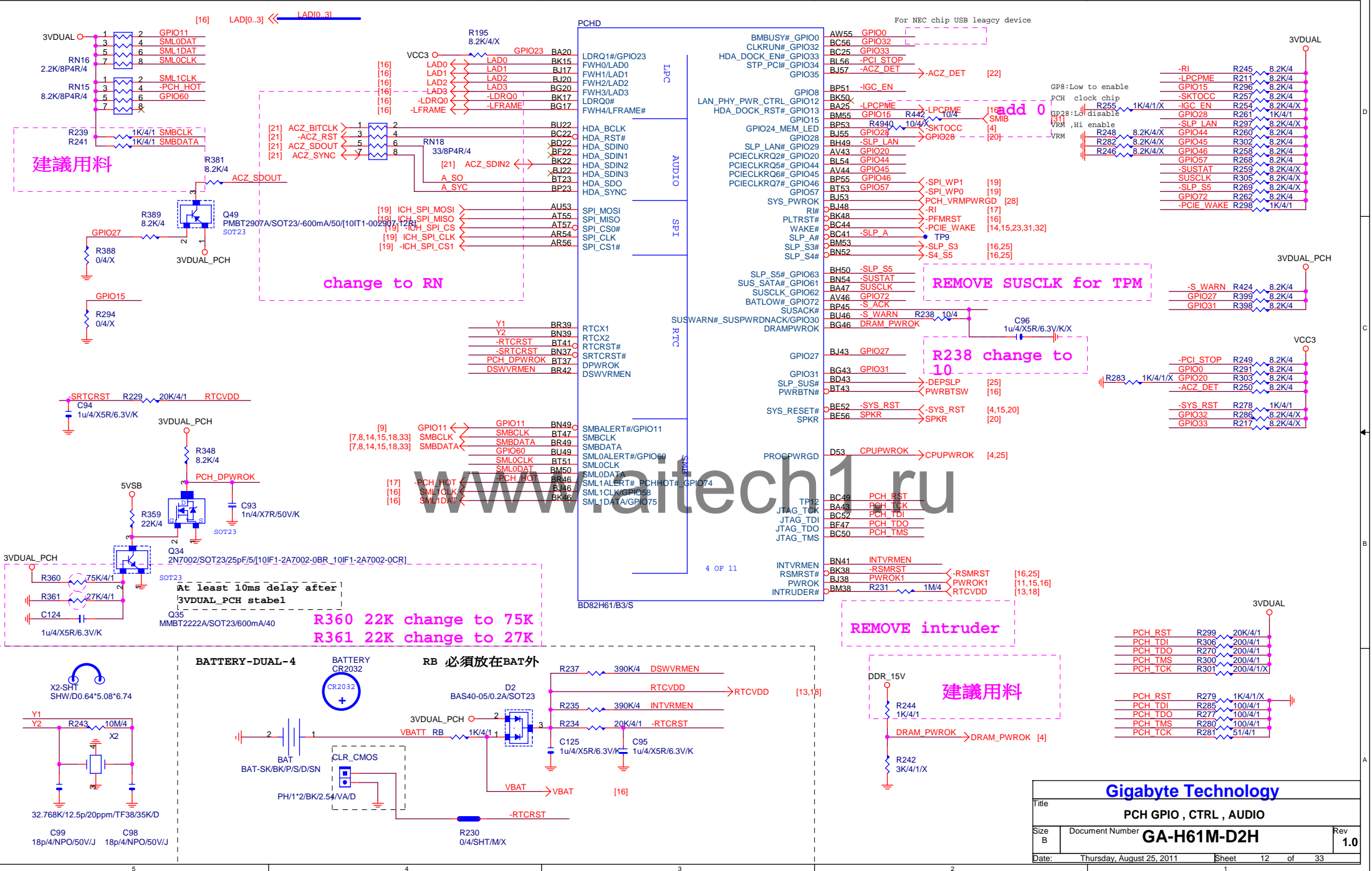
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| Gigabyte Technology | | | |
| Title | | | |
| CPU LGA1156-B | | | |
| Size | | | |
| Custom | | | |
| Document Number | | | |
| GA-H61M-D2H | | | |
| Date: | | | |
| Thursday, August 25, 2011 | | | |
| Sheet | | | |
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| Rev | | | |
| 1.0 | | | |

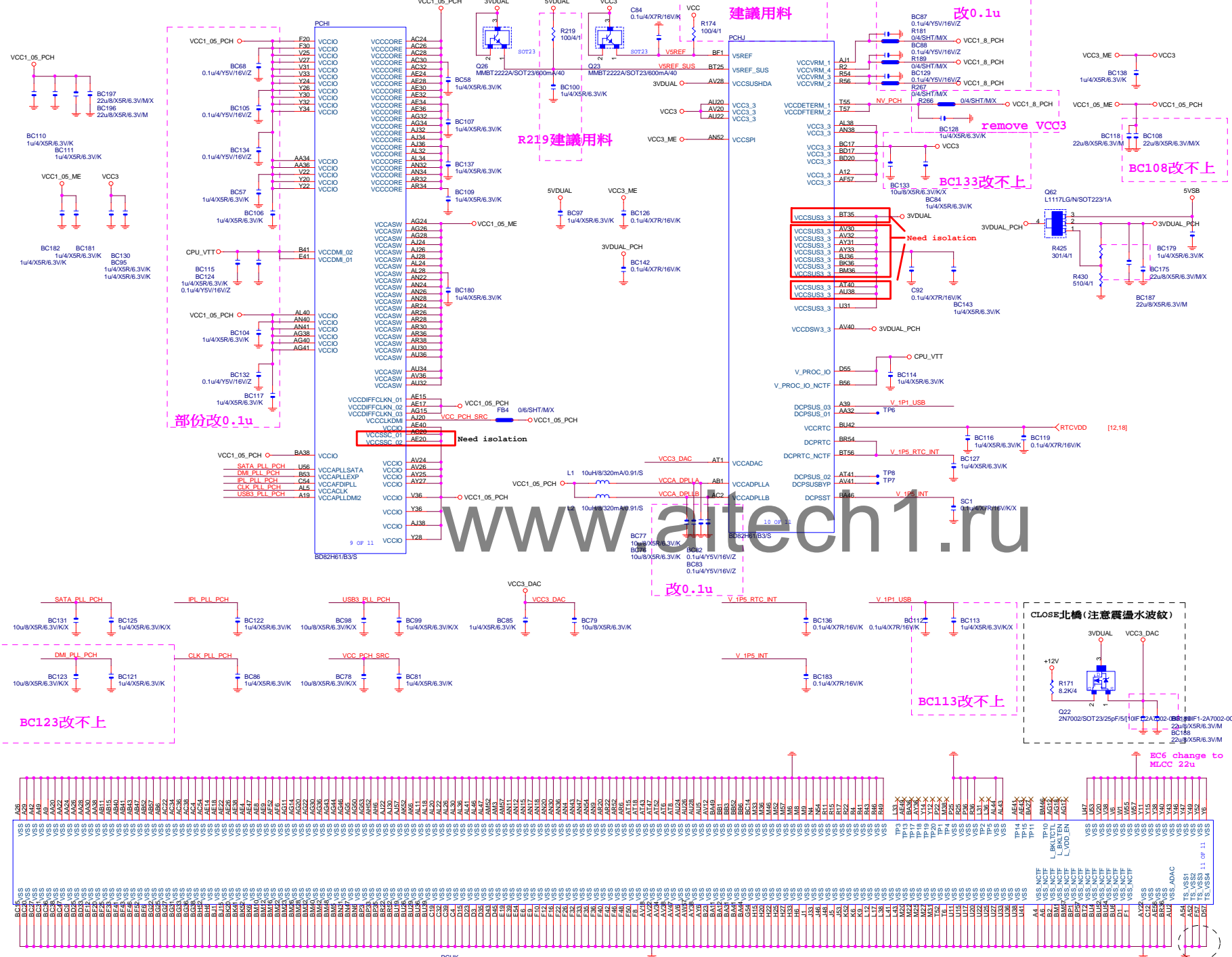




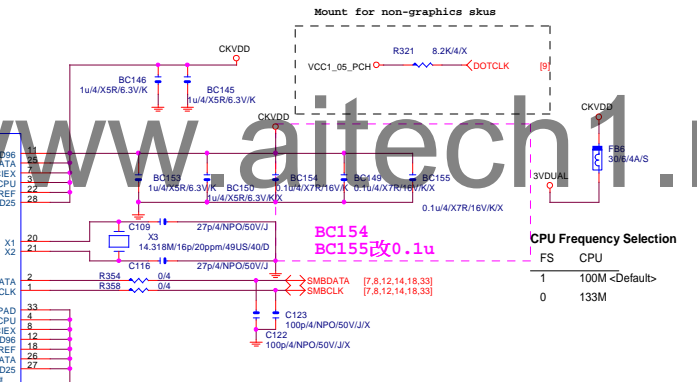
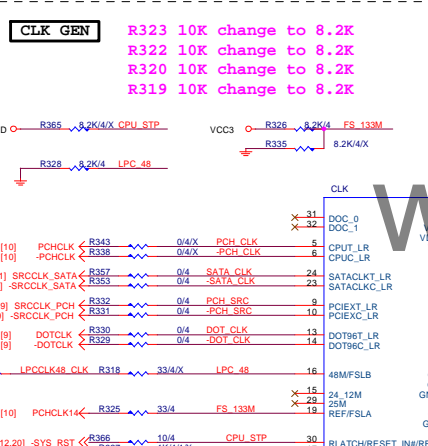
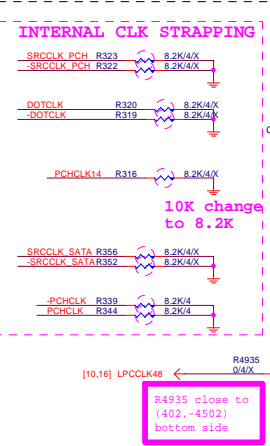
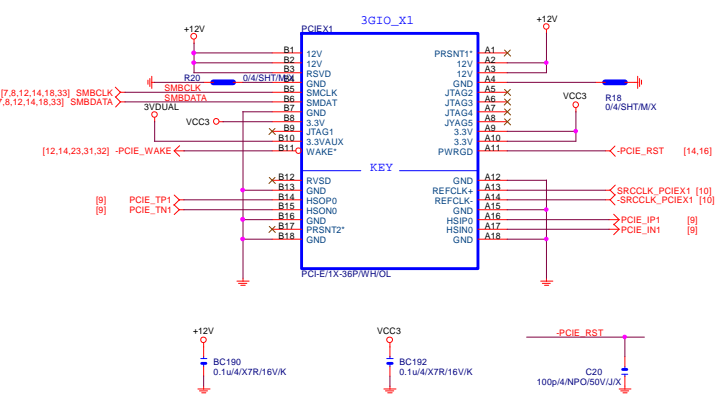








PCIE X1



CPU Frequency Selection

| FS | CPU |
|----|----------------|
| 1 | 100M <Default> |
| 0 | 133M |

GP22 Default GP22 DIOD8

GP23 Default CPU_PG DOD8

REMOVE R146,R149

powerflow change to 3VDUAL_PCH

R109 0 change to 10

-PFMRST2 for TPM

REMOVEQ20 R158 & R155 for ITE 8728 DX

| IT8728 | |
|--------|---------------------------|
| PIN121 | VCORE_EN#/PCR_C0 |
| PIN120 | VLDT_EN#/PCR_D0 |
| PIN19 | ATXPG |
| PIN31 | PCR_C1 |
| PIN53 | SST/AMDTSI_D/MTRB#/PCR_D1 |
| PIN55 | PECI/AMDTSI_C/DRV# |
| PIN66 | SYS_3VSB |
| PIN70 | GP47 |
| PIN95 | VIN2 (VCC5) |
| PIN96 | VIN1 (VCC12) |
| PIN97 | VIN1/VDIMM_STR (1.5V) |
| PIN98 | VIN0/VCORE (1.1V)/NC |

REMOVE R61 & R141 .R77 R136 改上件 for ITE 8728 DX

R83 R66 change to 10

IT8728F(GB)

ADD LPT PORT

REMOVE ON/OFF CHARGER

GP22 for LAN AR8151

GP40 Default 3VBSW# DO8

CHK GPIO53

remove DTR1- & RTS1-

remove IO GP43 pull high

建議用料

建議用料

Gigabyte Technology

ITE 8728 LPC IO

GA-H61M-D2H

Rev 1.0

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REMOVE NR1A- in R1.1

建議用料

[18]

NRIB

R43
75K/4/1

R42
8.2K/4

SOT23

Q12
MMBT2222A/SOT23/600mA/40

[12]

KBDATA 1

KBCLK 6

MSDATA 3

MSCLK 4

CM1293A-04SO/S[10TA1-010009-10R_10TA1-018902-10R]

FUSEVCC_R1

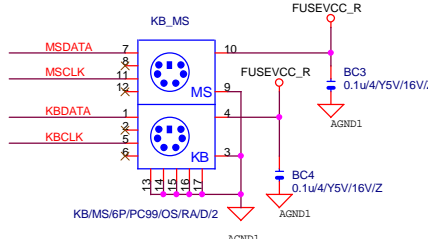
UR2
150K/4

UR3
270K/4

USB OC_R

[9,31]

KB/MS



[16]

[16]

[16]

[16]

KDAT 2
KCLK 4
MDAT 6
MCLK 8

KBDATA 1
KBCLK 3
MSDATA 5
MSCLK 7

FUSEVCC_R
RN1
8.2K/8P4R/4

CN1
180p/8P4C/6/NPO/50V/K

AGND1

-PROHOT

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Q16
BAT54A/SOT23/200mA

R39
8.2K/4

R46
0/4

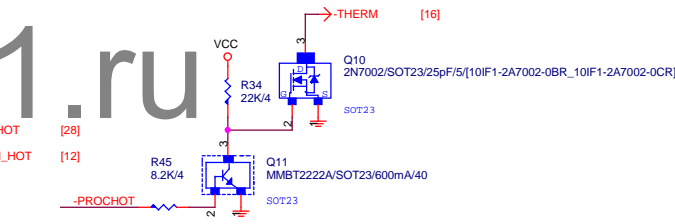
VR_HOT

PCH_HOT

[4]

[28]

[12]



for proshot
R100 1.2K change to 1.87K

deasserted at 116 degree

R_USB

EMI request

5VDUAL

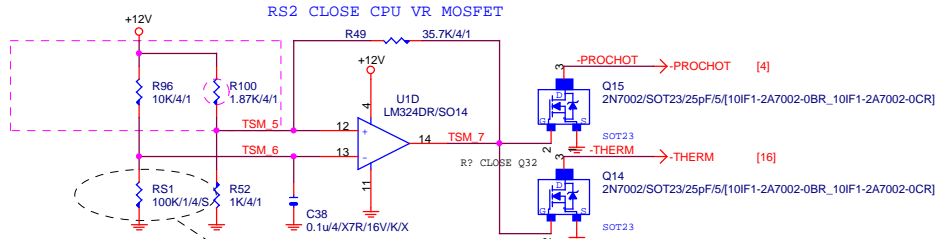
F9 Change to
SMD1206P200SLR/S

SMD1812P260/6V

FUSEVCC_R

UR1
0/6

AGND1

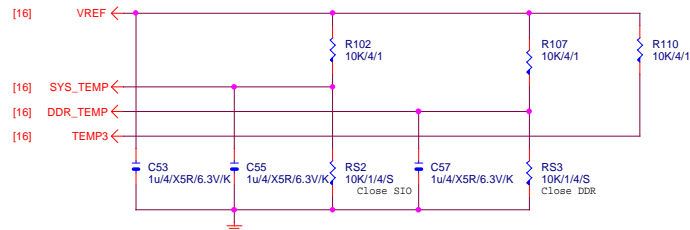


CLOSE PWM HOT MOSFET

Gigabyte Technology

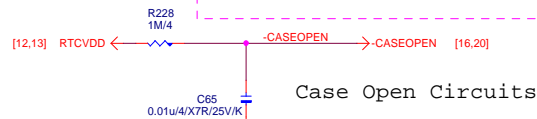
| | | | |
|------------------------------------|--------------------------------|------------|--|
| Title KB_MS,R_USB,-PROHOT,RI | | | |
| Size Custom | Document Number GA-H61M-D2H | Rev 1.0 | |
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TEMP H/W MONITOR

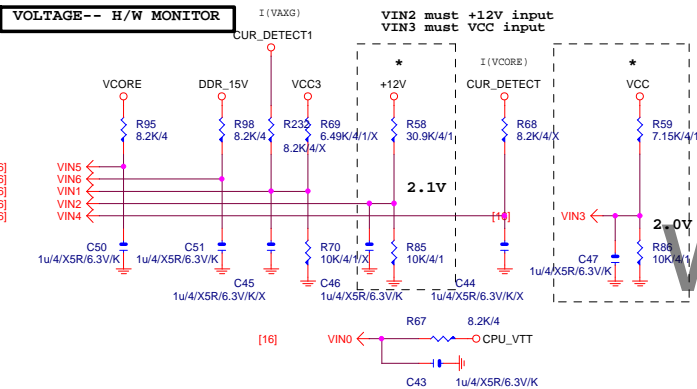


CASE OPEN

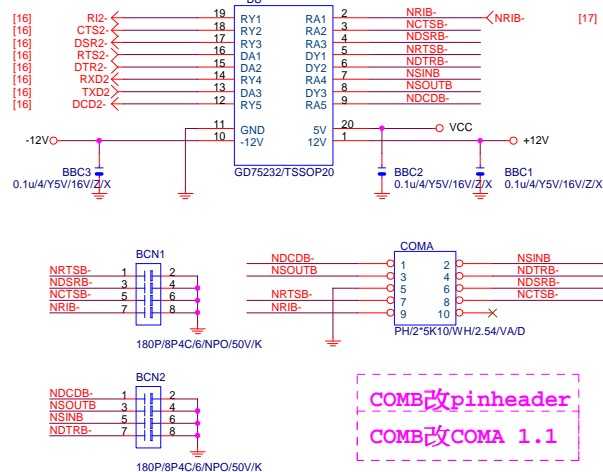
REMOVE intruder



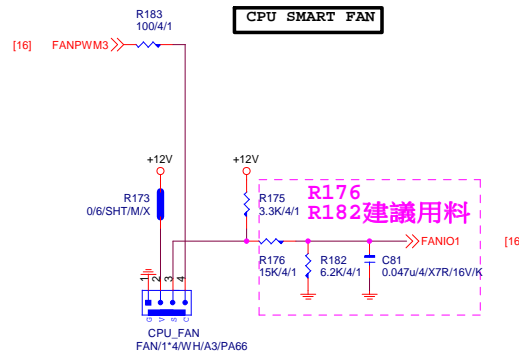
VOLTAGE-- H/W MONITOR



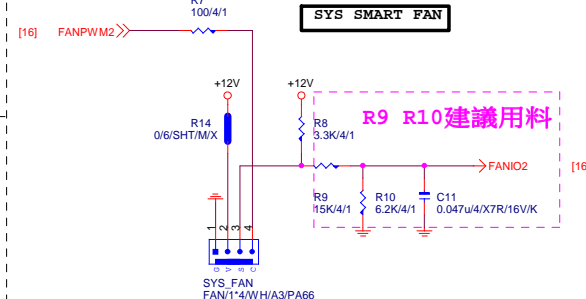
COMB



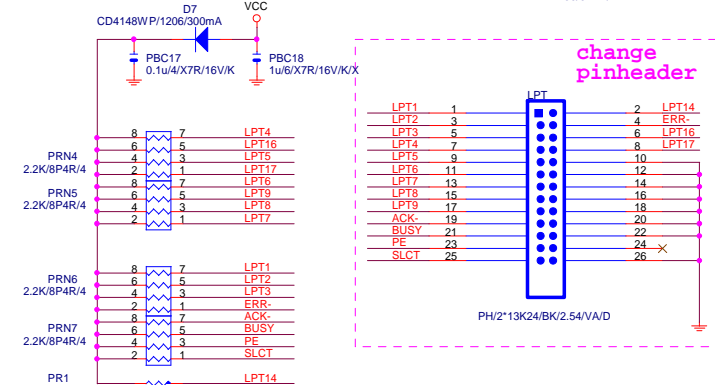
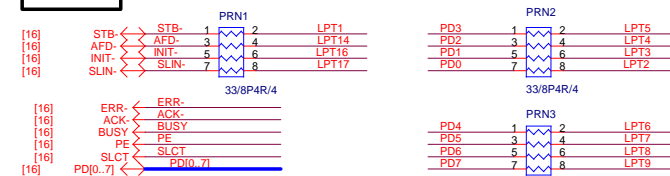
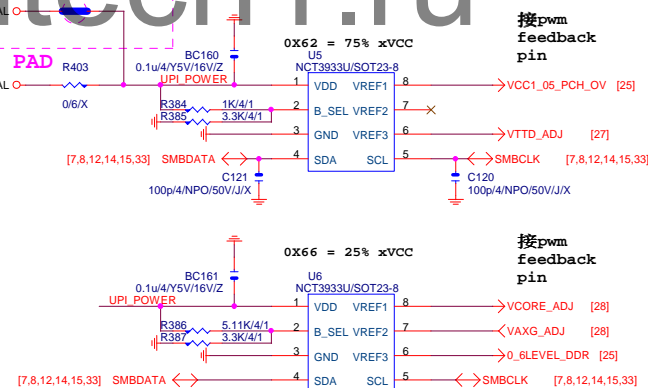
CPU SMART FAN



SYS SMART FAN



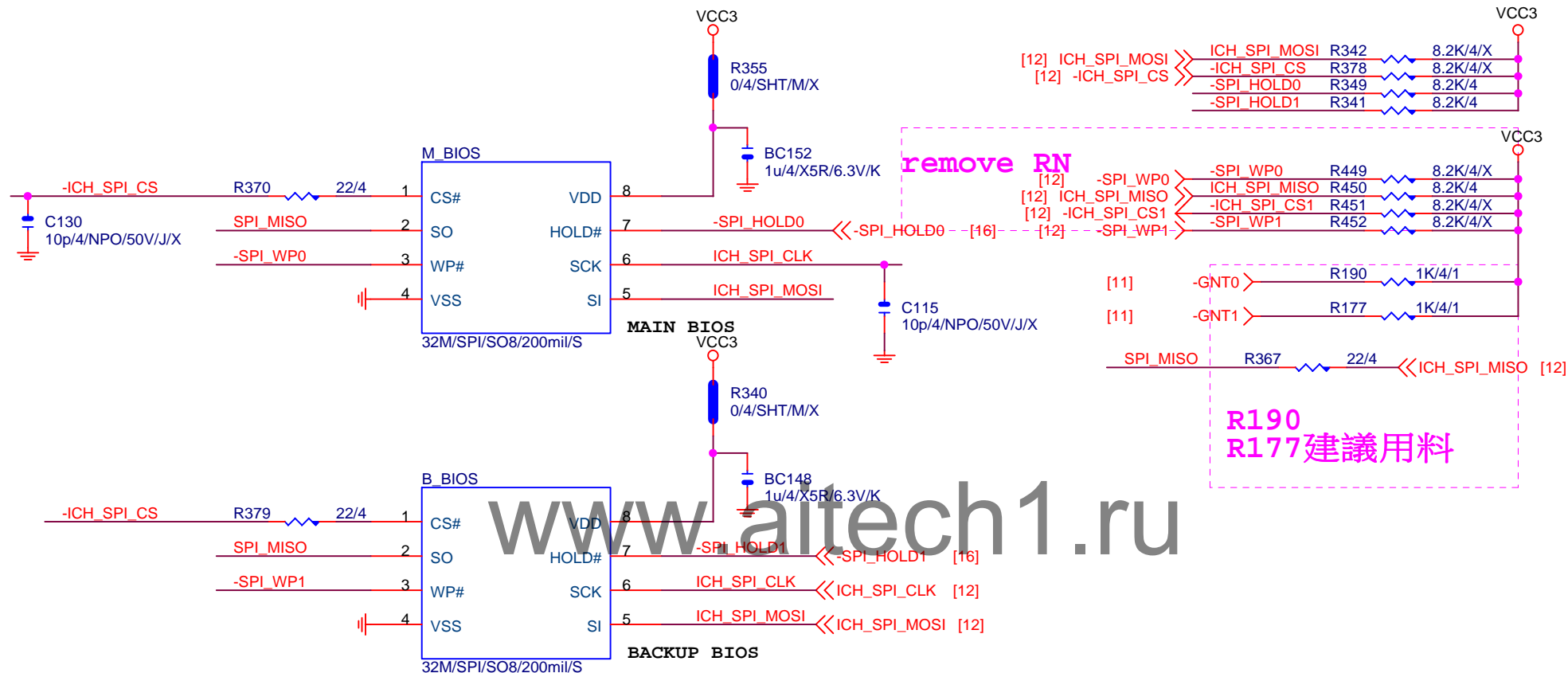
LPT PORT

[illegible]

COMB改pinheader
COMB改COMA 1.1

DUAL BIOS

R349 R341 改上件 for ITE 8728 DX



| BOOT DEVICE | GNT1 | GNT0 |
|-------------|------|------|
| LPC | 0 | 0 |
| PCI | 0 | 1 |
| SPI | 1 | 1 |

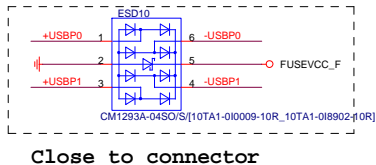
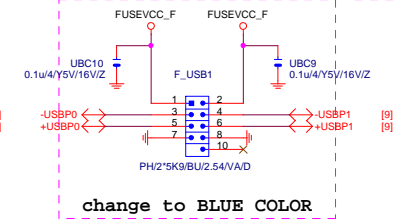
1 means floating
0 means PD 1K

Gigabyte Technology

| | | |
|-----------|---------------------------|----------------|
| Title | | |
| DUAL BIOS | | |
| Size A | Document Number | Rev |
| | GA-H61M-D2H | 1.0 |
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FRONT USB1

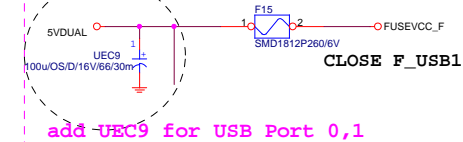
FU改pinheader



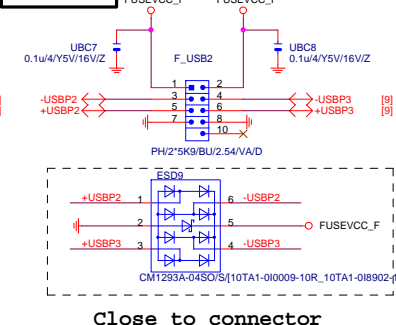
SATA LED

[11] -SATALED -HDLED

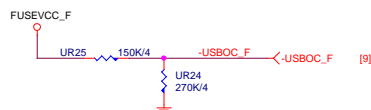
CLOSE R_USB



FRONT USB2

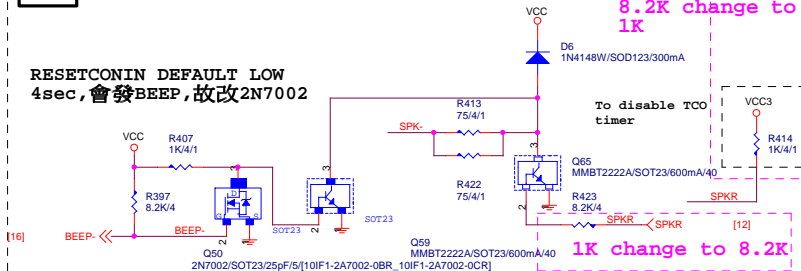


FU改pinheader



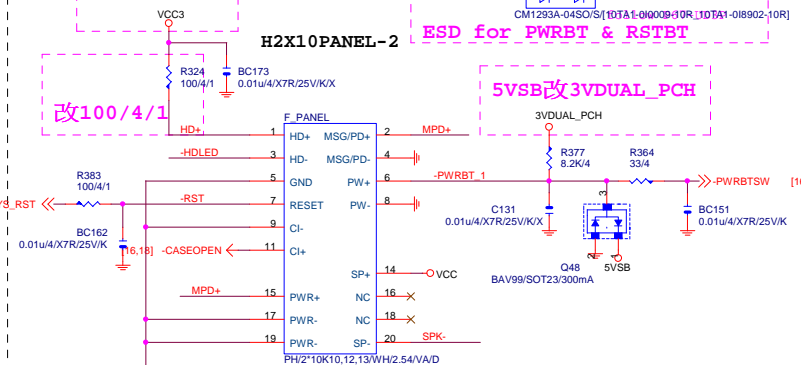
SPKR

RESETCONIN DEFAULT LOW
4sec, 會發BEEP, 故改2N7002

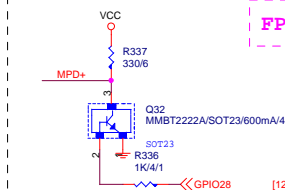


INTEL FRONT PANEL

改VCC3




FP改pinheader

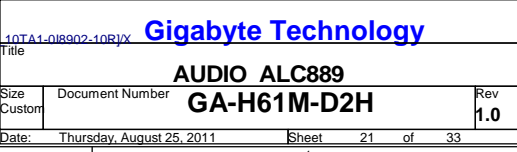


| Gigabyte Technology | | | |
|---------------------|---------------------------|----------------|---------|
| Title | FP,F_USB,SPKR,SATA LED | | |
| Size | Document Number | GA-H61M-D2H | |
| Custom | | | Rev 1.0 |
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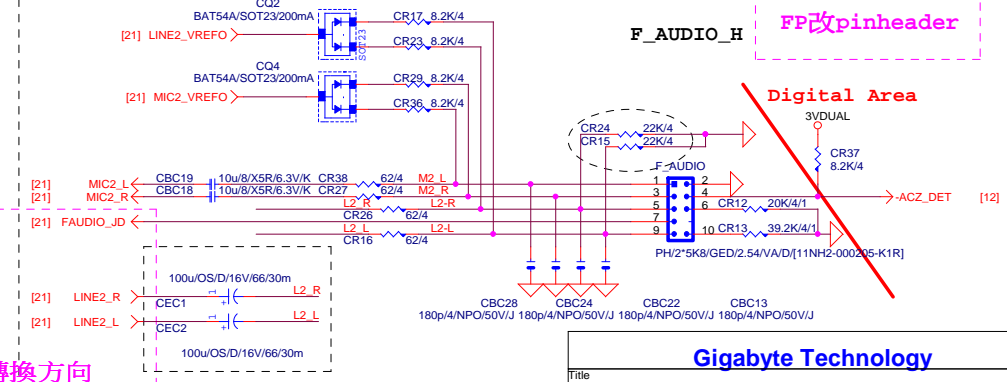
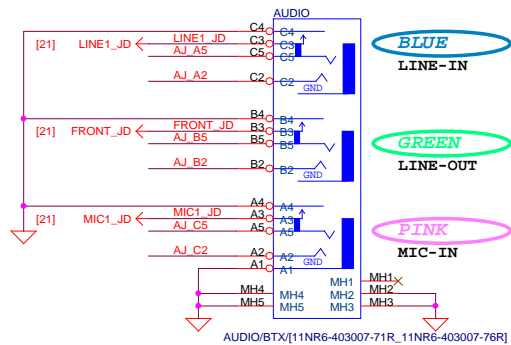
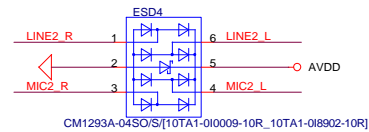
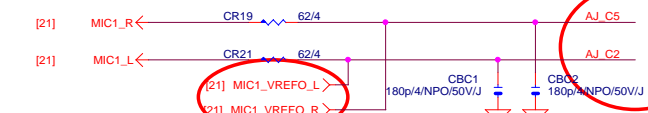
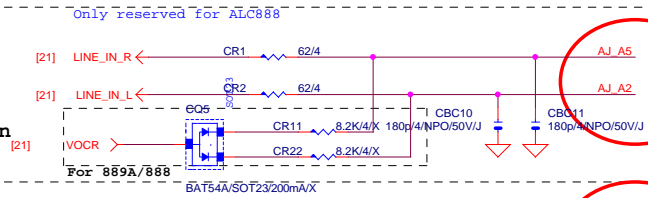
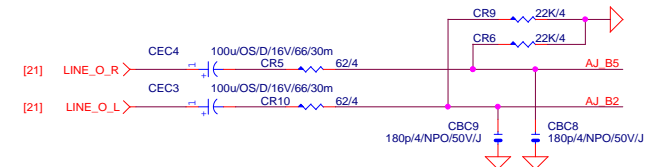
CR28: 20K/4/0.1% @ALC889A
CR28: 20K/4/1% @others



The diagram shows a resistor labeled CR28 connected to a ground symbol. The resistor is labeled with its value 20K/4/1.



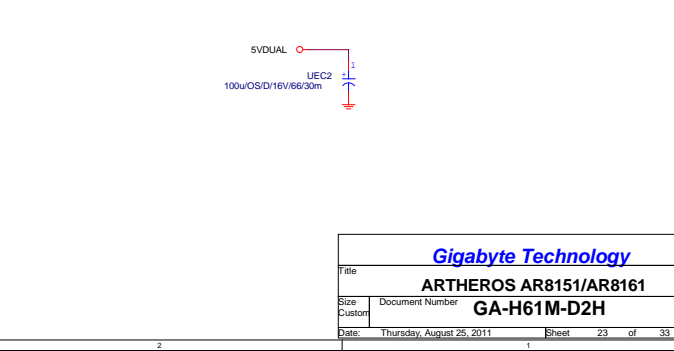
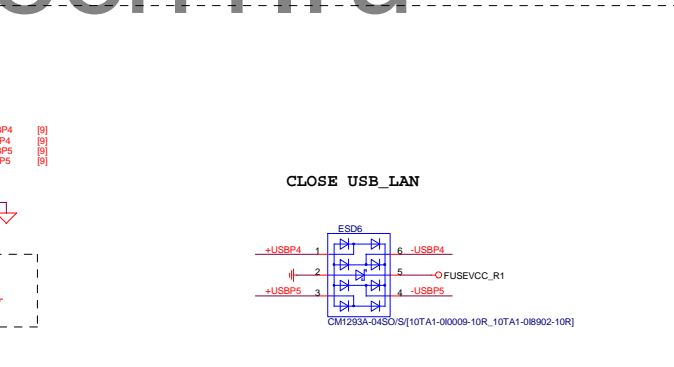
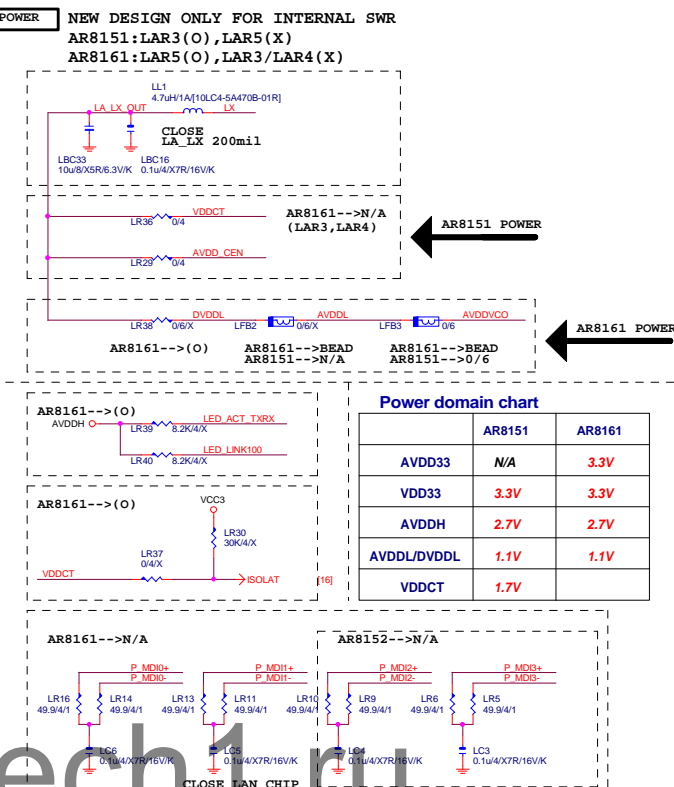
change to DGND

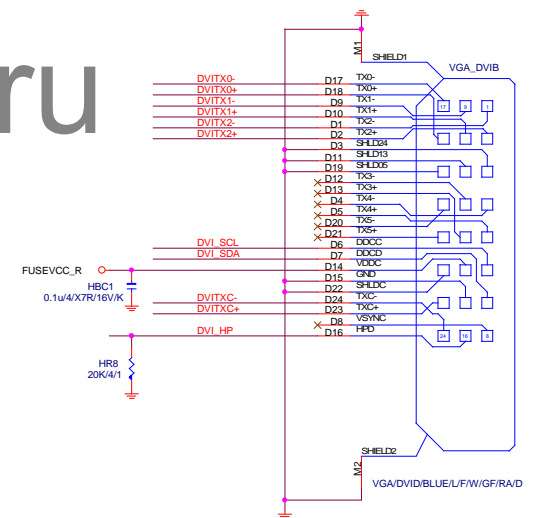
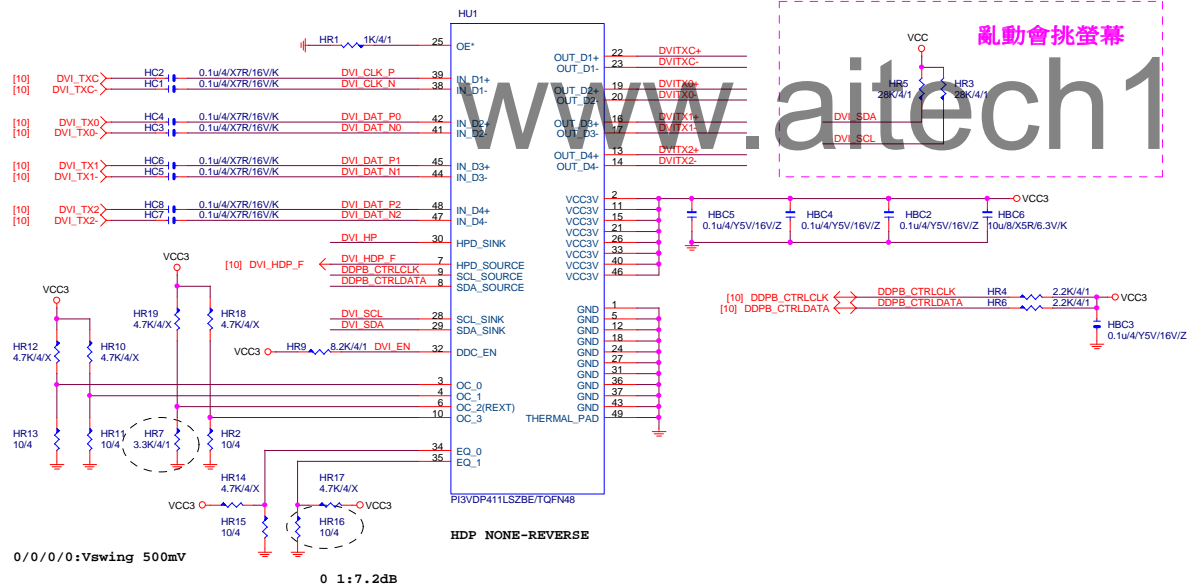
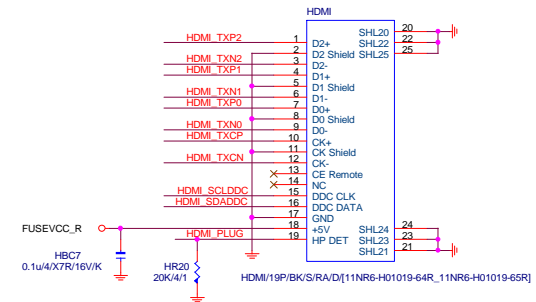
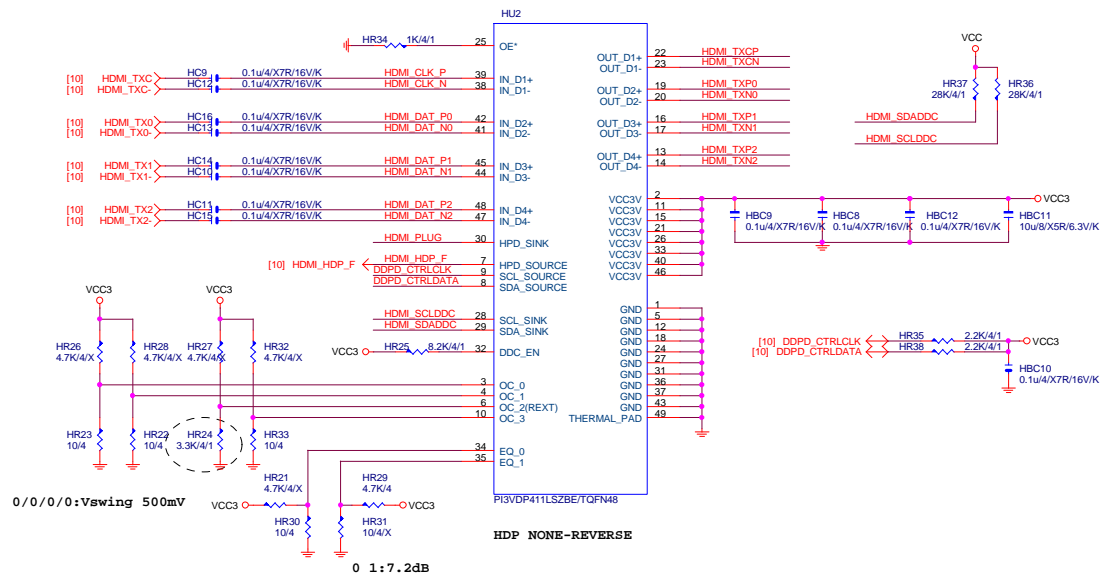


轉換方向

Gigabyte Technology

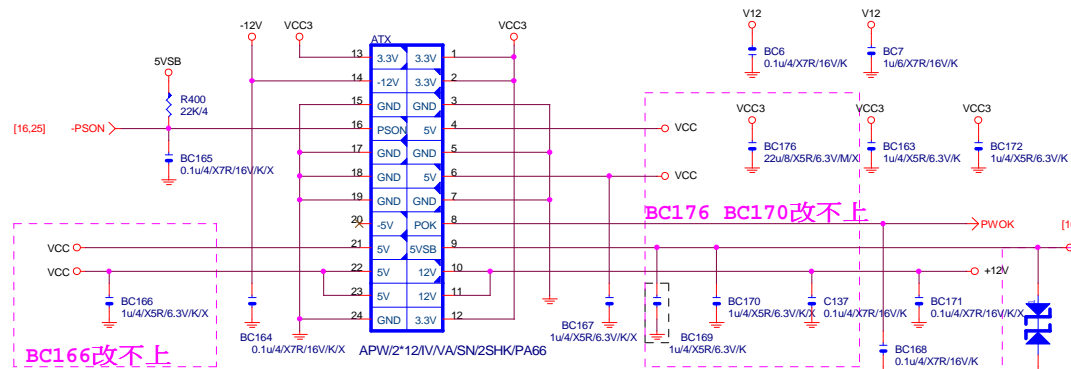
| | | | |
|-------------|---------------------------|----------------|---------|
| Title | | | |
| AUDIO JACK | | | |
| Size Custom | Document Number | GA-H61M-D2H | Rev 1.0 |
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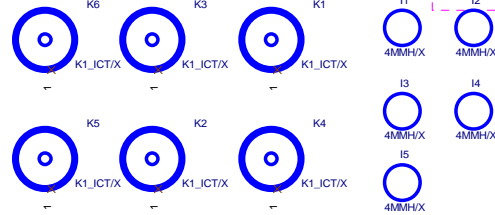
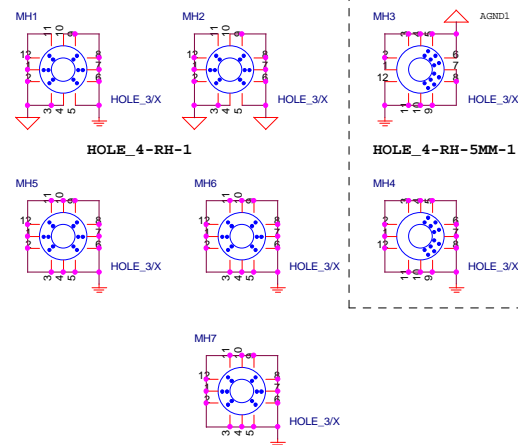
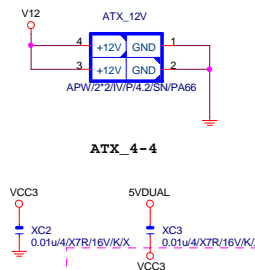


| Gigabyte Technology | | | |
|---------------------|---------------------------|-------|----------|
| Title | | | |
| HDMI / DVI | | | |
| Size | Document Number | Rev | |
| Custom | GA-H61M-D2H | 1.0 | |
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ATXX24 POWER CONNECTOR



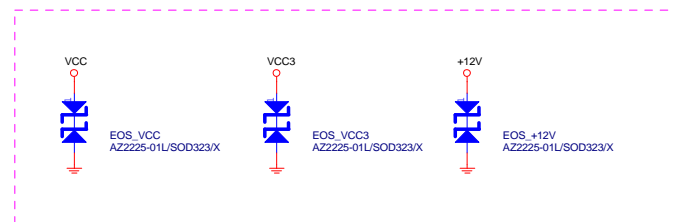
ATXX4 POWER CONNECTOR



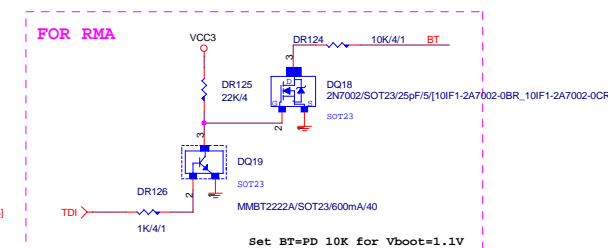
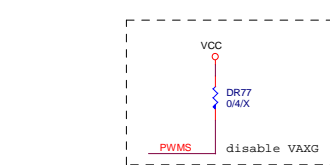
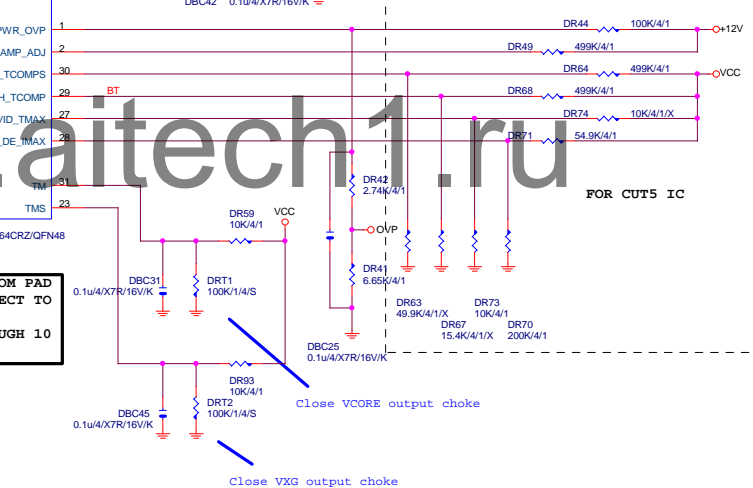
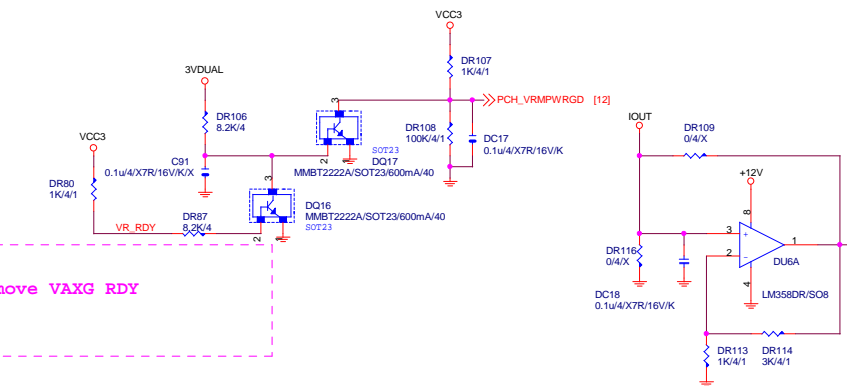
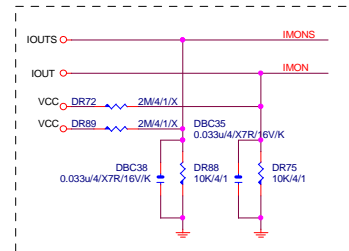
```
| To prevent the 5VSB  
| under loading when  
| boot
```

5VDUAL1(USB PORT/DDR111 POWER)
5VDUAL(3VDUAL/OTHER)

```
-S_WARN-->5VDUAL1-->-S_ACK(PCH)-->-DEPSLP/-RSMRST-->5VDUAL-->3VDUAL
```

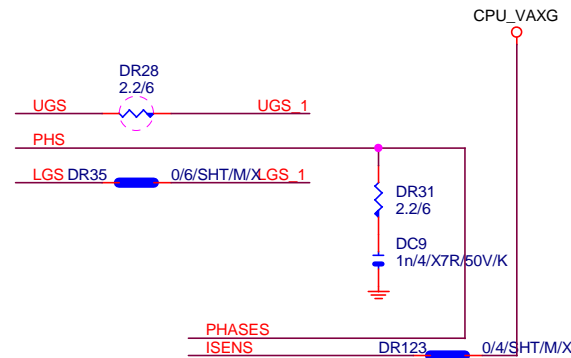
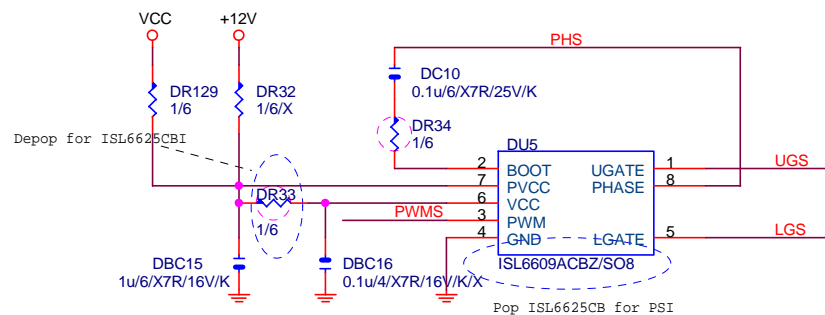



```
remove VAXG RDY
```

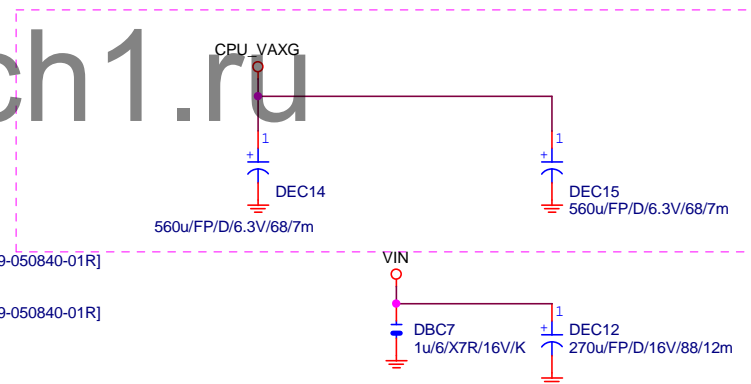
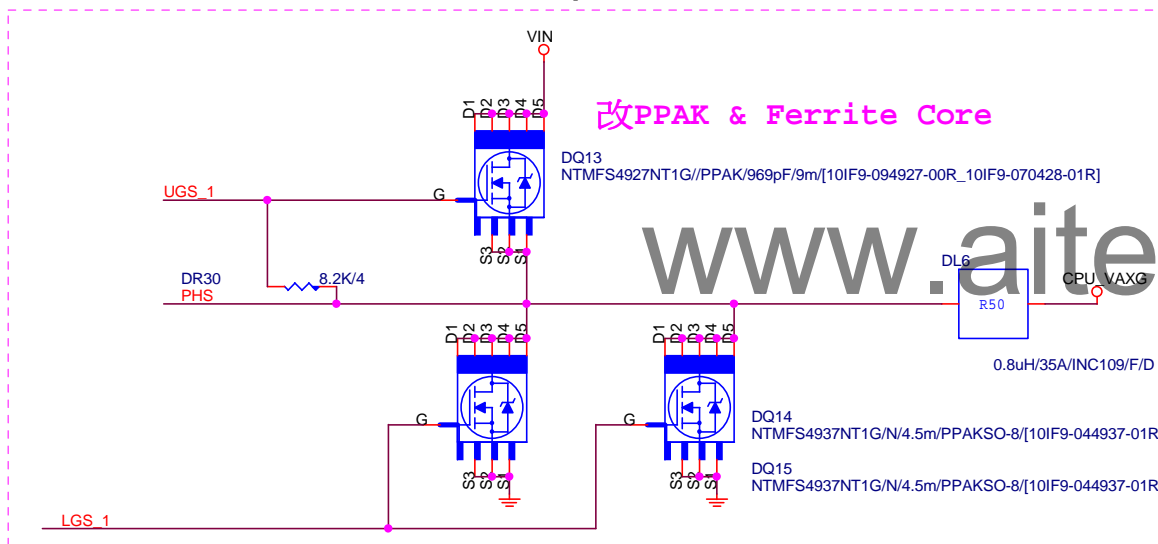


VAXG

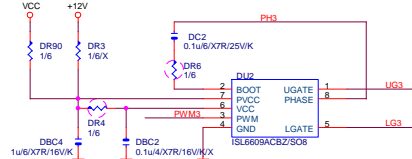
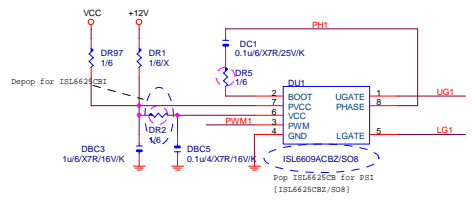
DR33 0 change to 1
DR34 0 change to 1
DR28 0 change to 2.2



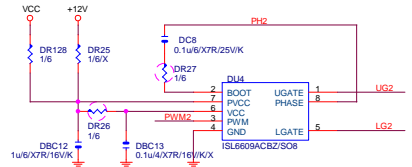
PWMS → PWMS [28]
ISENS → ISENS [28]
PHASES → PHASES [28]



DR5 0 change to 1
 DR2 0 change to 1
 DR4 0 change to 1
 DR6 0 change to 1



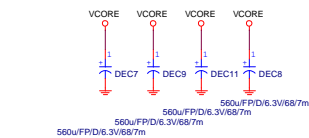
DR20 0 change to 1
 DR19 0 change to 1
 DR26 0 change to 1
 DR27 0 change to 1



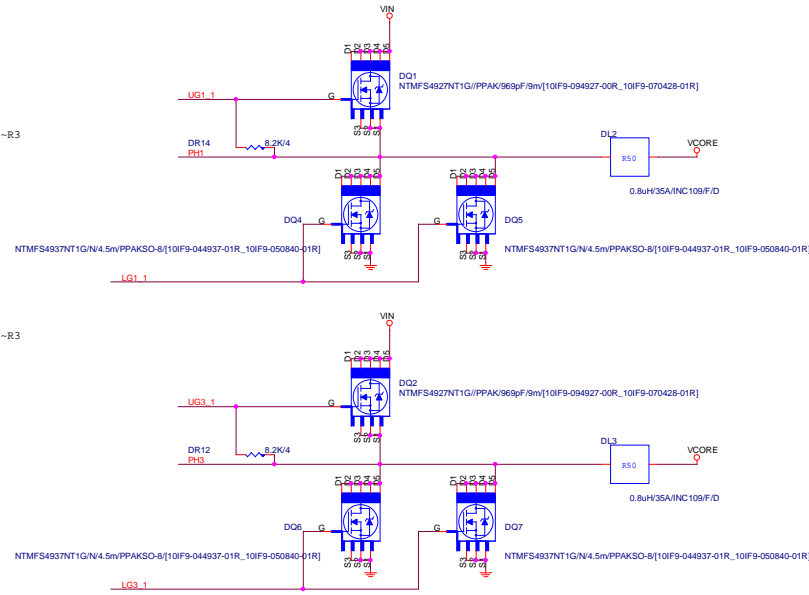
[1]

[3]

[2]

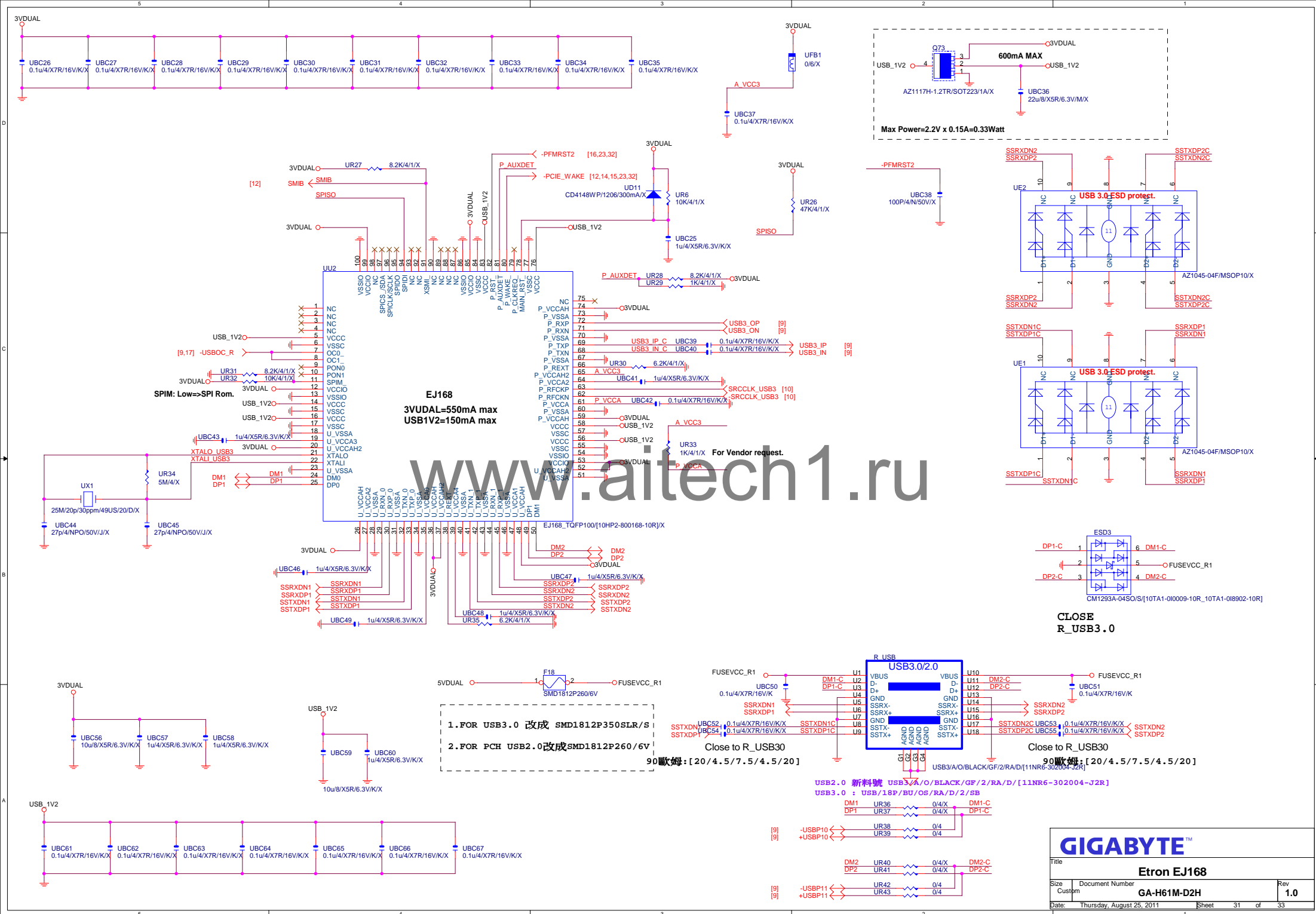


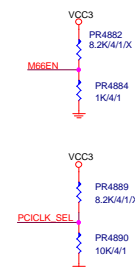
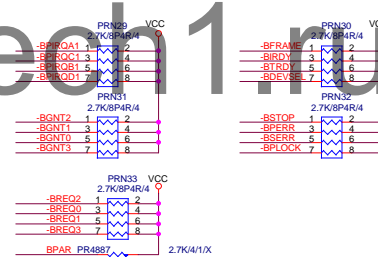
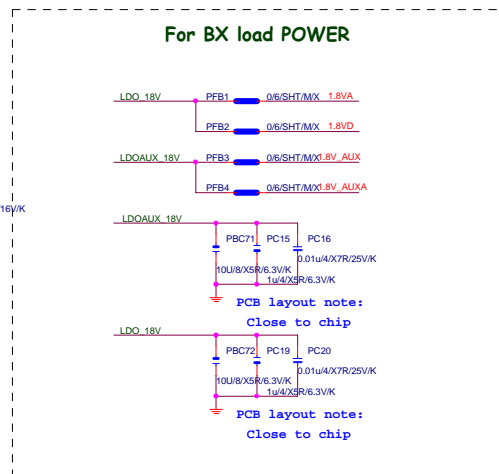
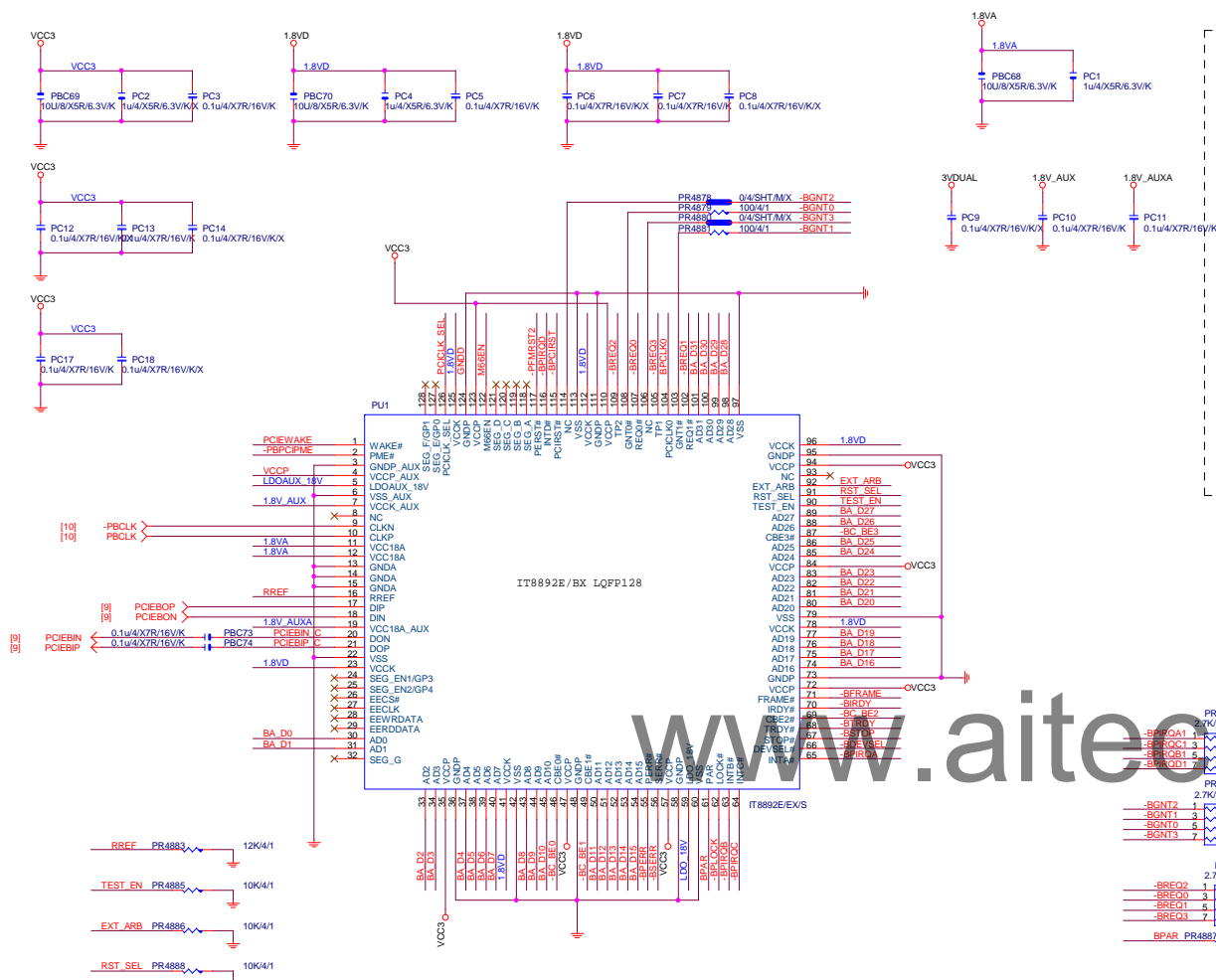
改PPAK & Ferrite Core



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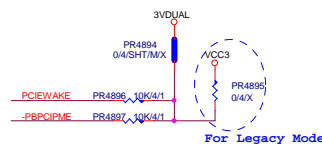
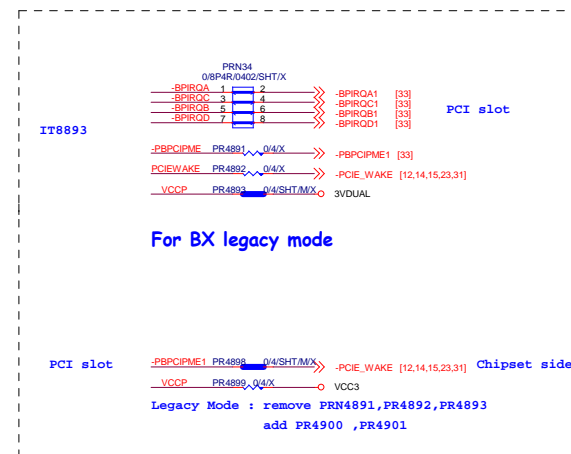
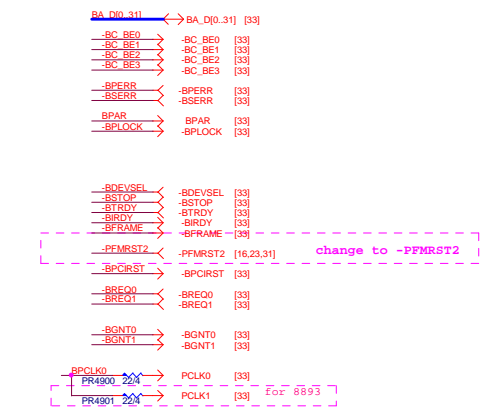
| Gigabyte Technology | | | |
|---------------------------|-----------------|-------------|-------|
| CPU CORE VR-3 | | | |
| File | Document Number | GA-H61M-D2H | |
| Size | Custom | Date | Rev |
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| 1.0 | | | |





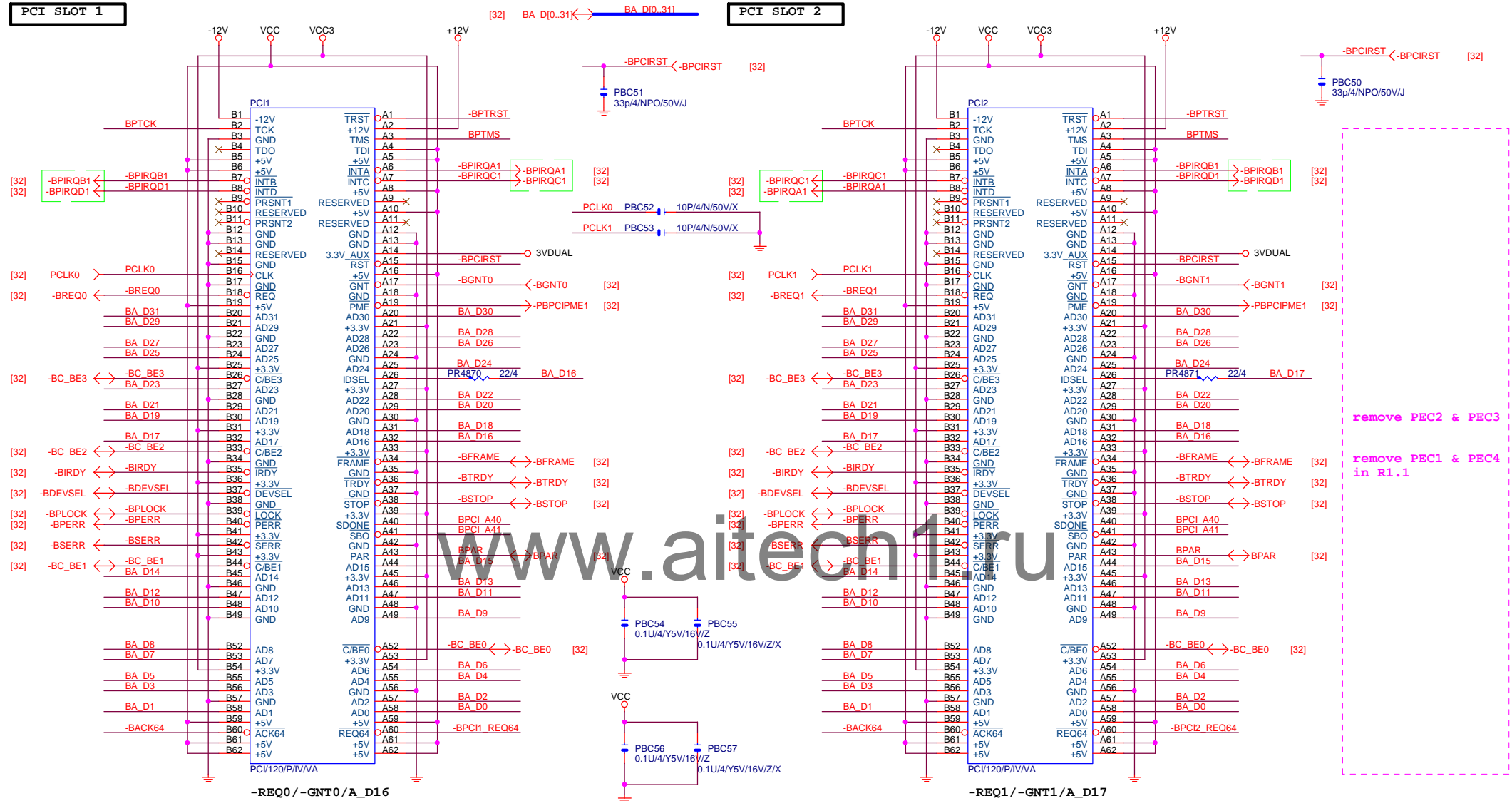
High: Enable PCI CLK 66MHz
Low: Disable PCI CLK 66MHz

High: PCICLK INPUT form CLK Gen
Low: PCICLK OUTPUT form IT8893 chip



PCI SLOT 1

PCI SLOT 2



remove PEC2 & PEC3
in R1.1

GIGABYTE™

PCI SLOT 1&2

| | | |
|---------------------------------|-----------------|----------|
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| B | GA-H61M-D2H | 1.0 |
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